

FIG. 1

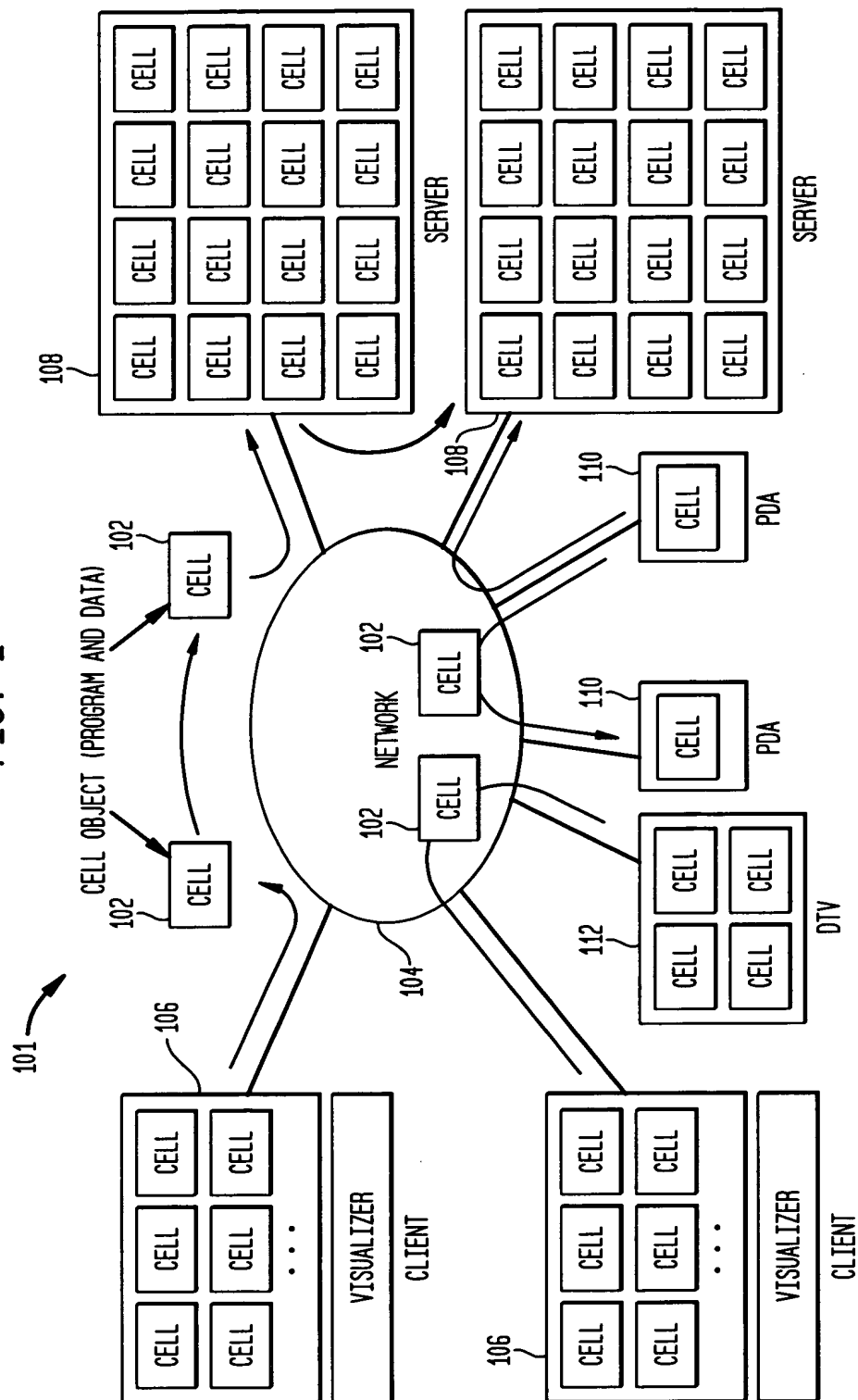


FIG. 2

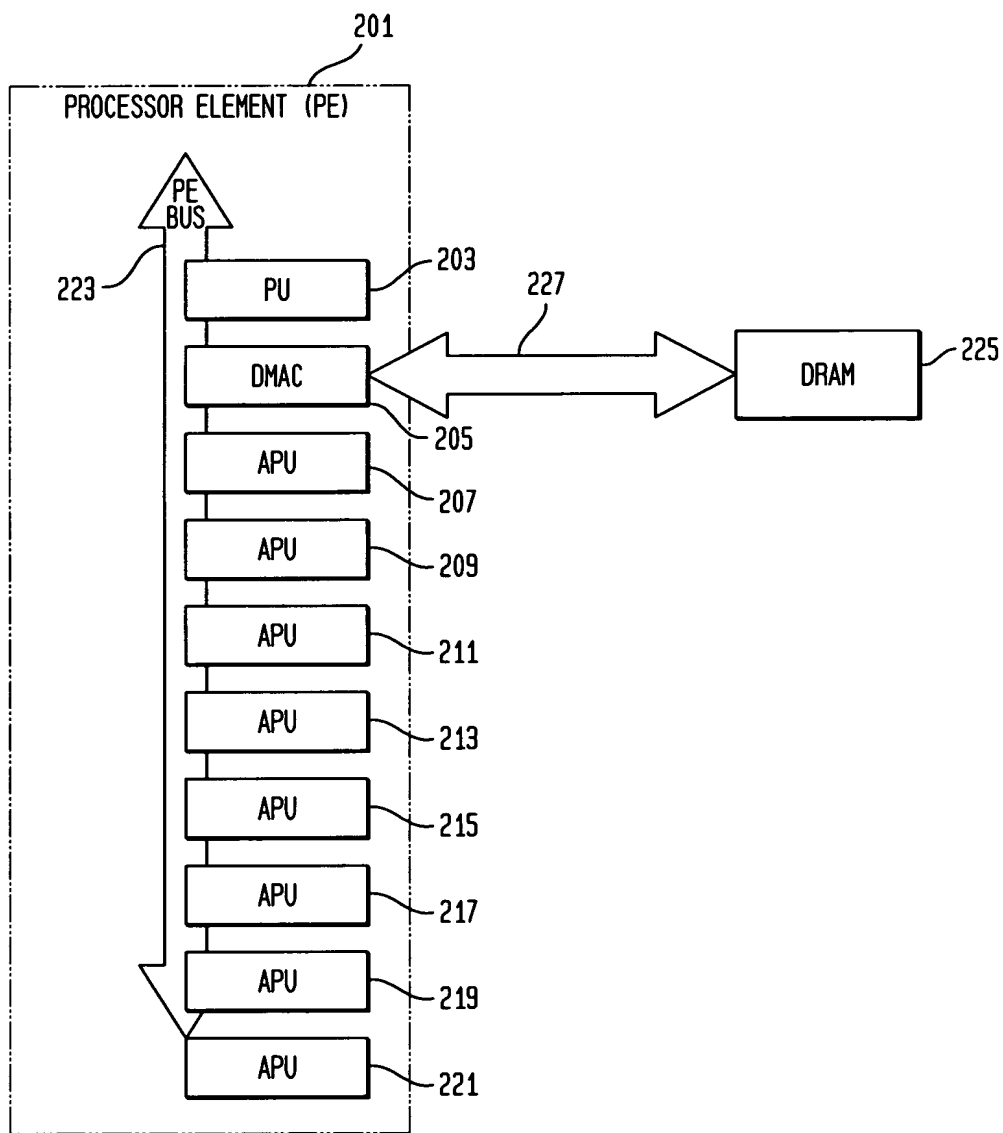


FIG. 3

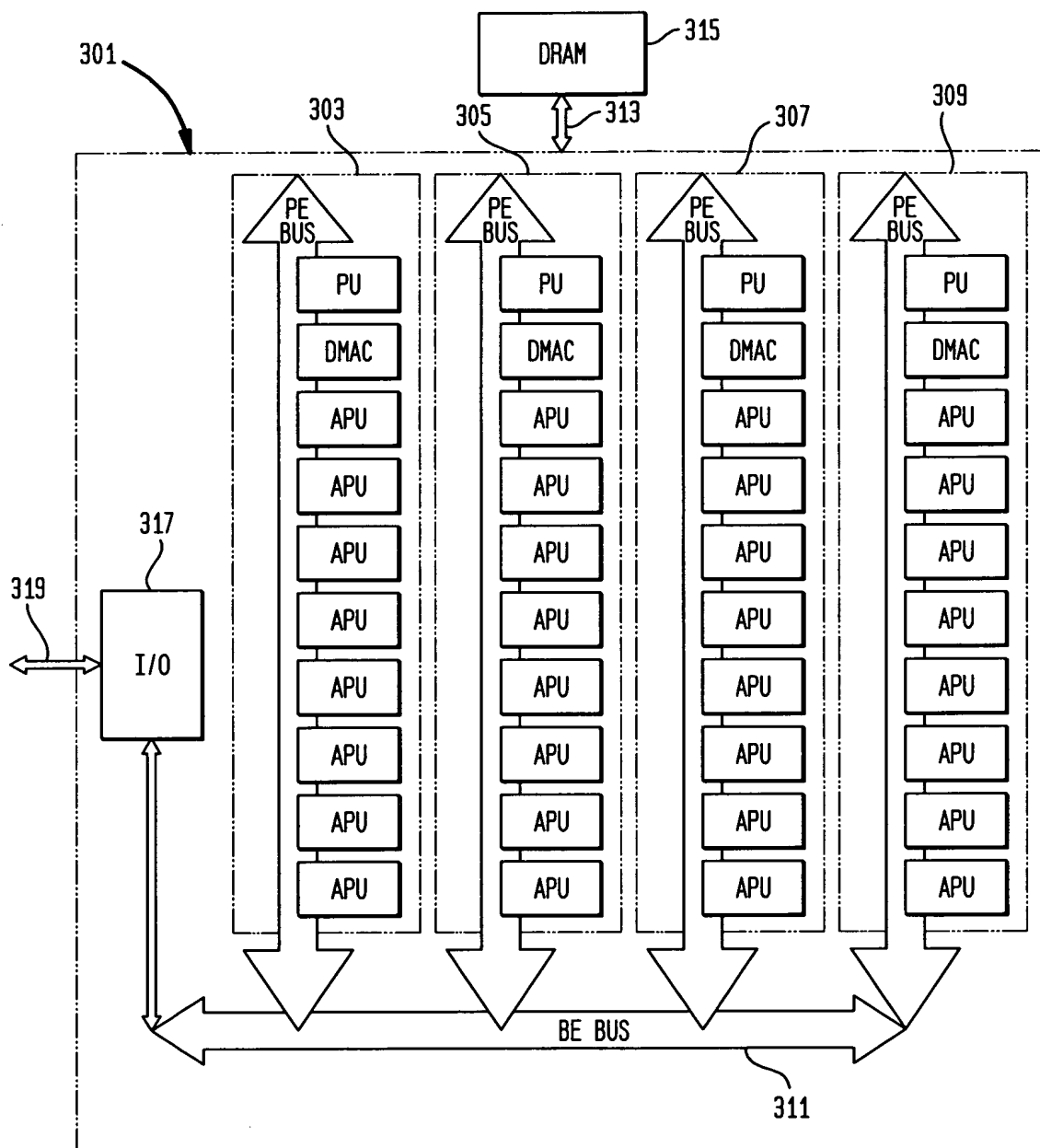


FIG. 4

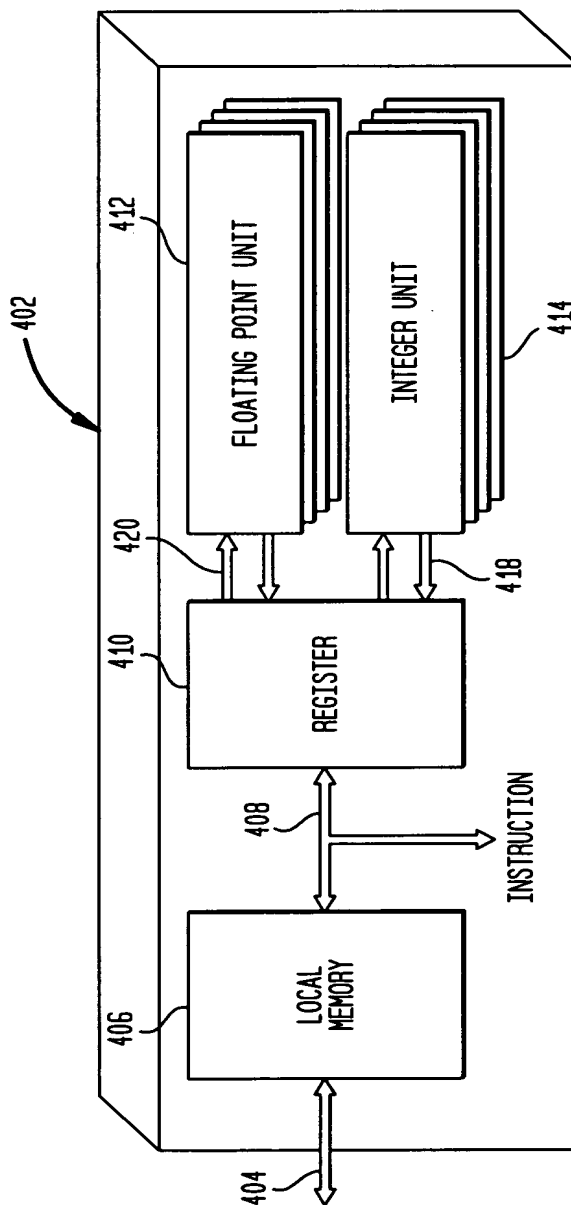
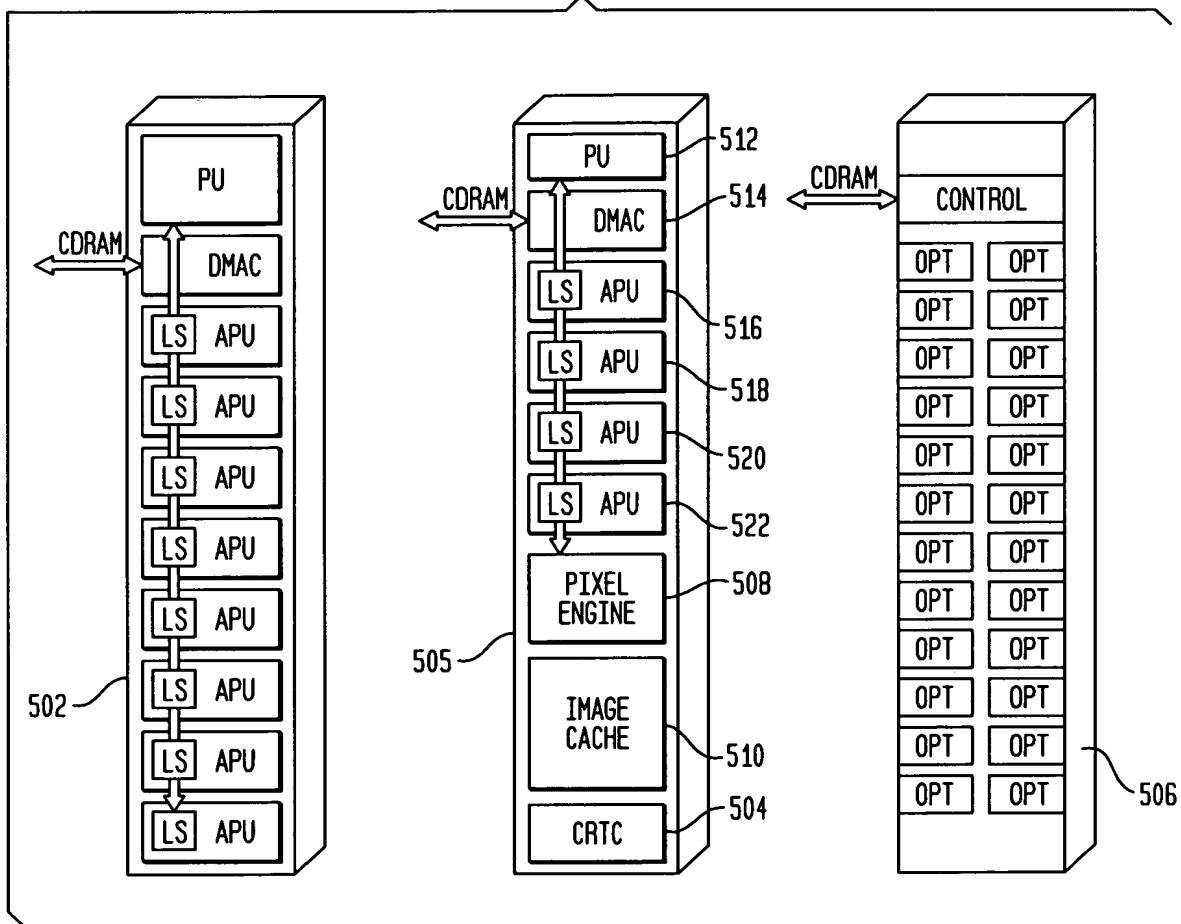
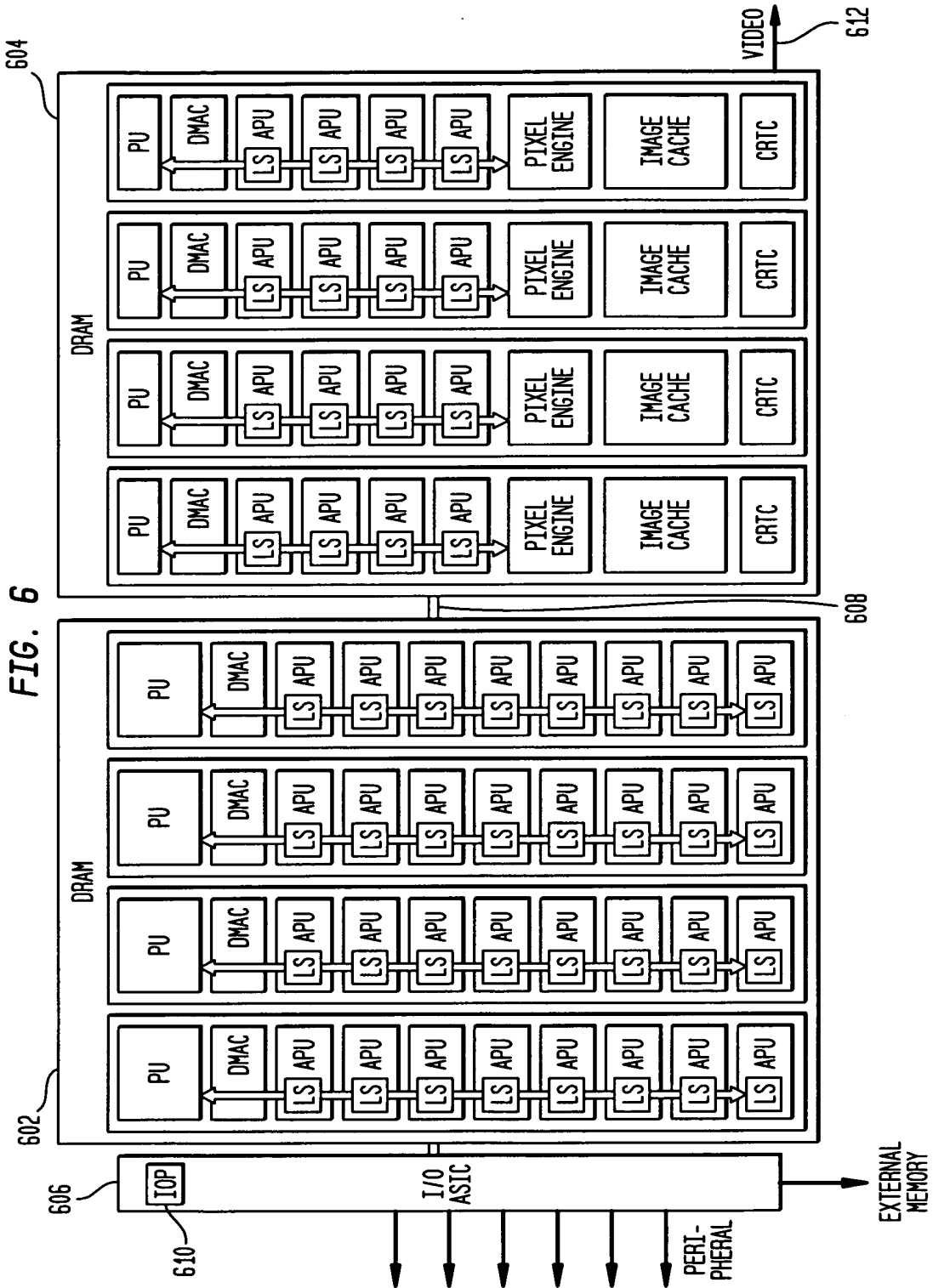


FIG. 5





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FIG. 7

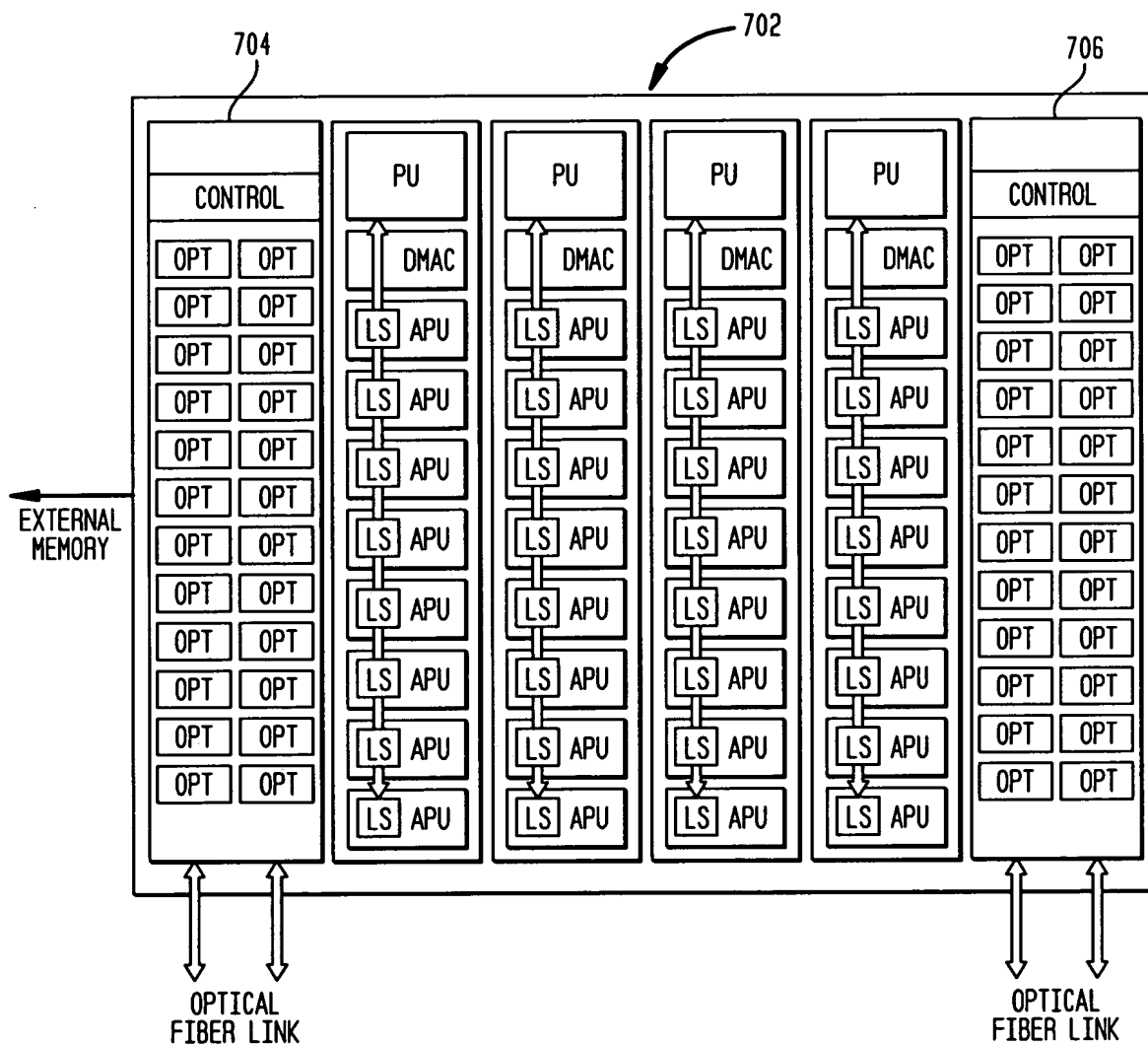


FIG. 8

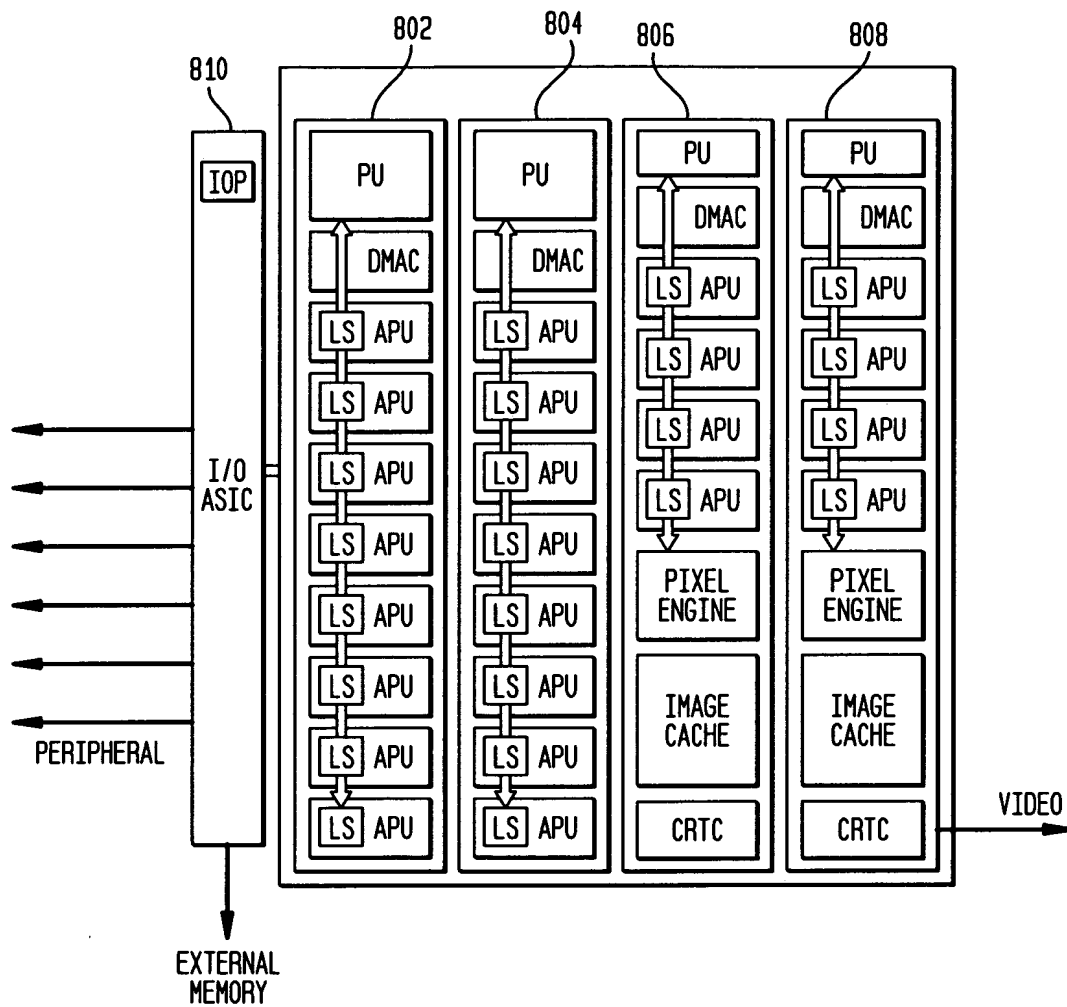




FIG. 9

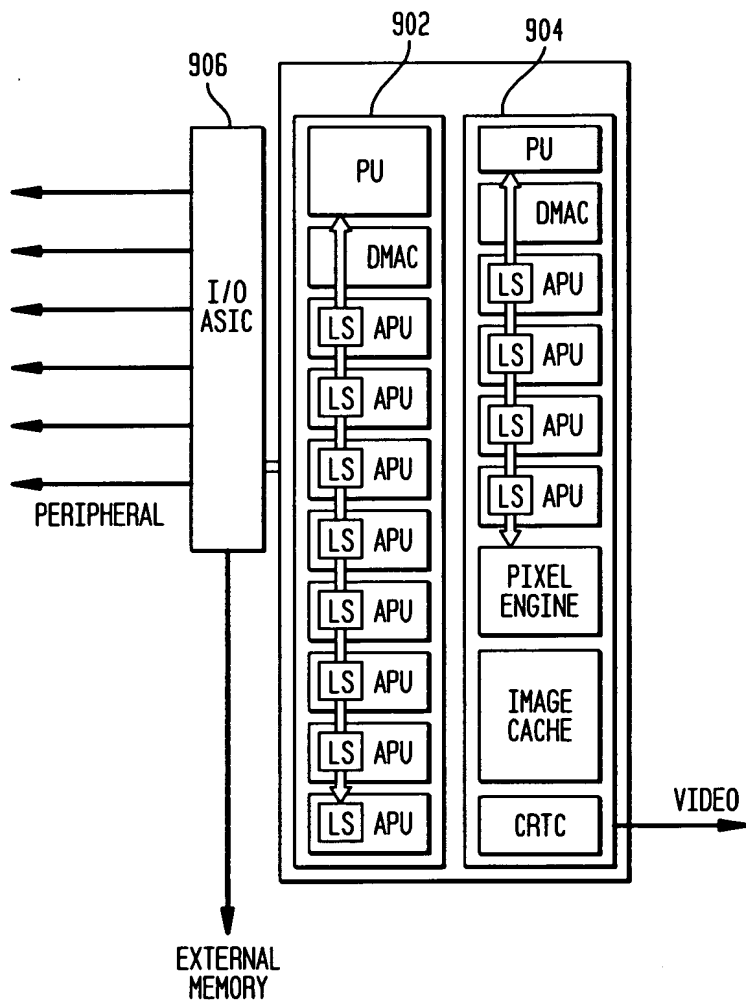


FIG. 10

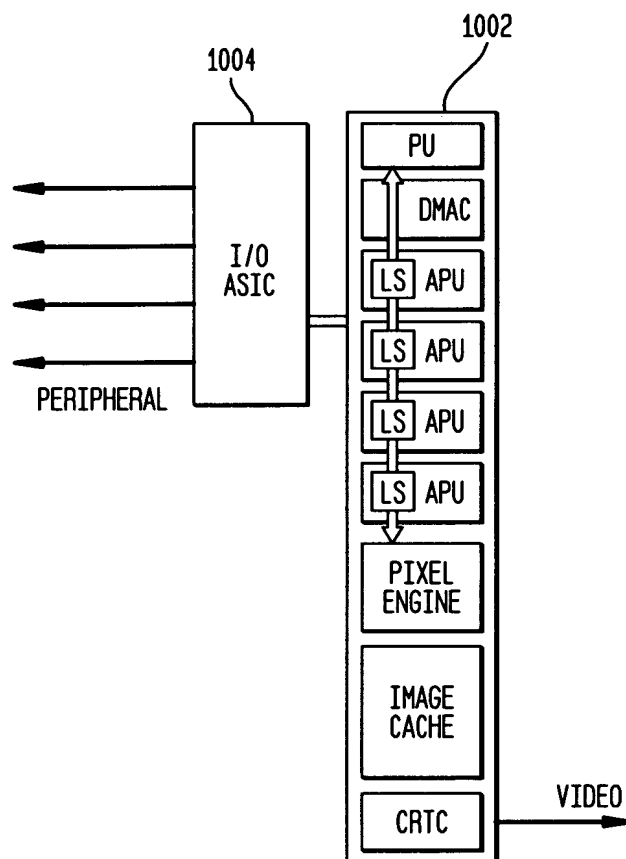


FIG. 11A

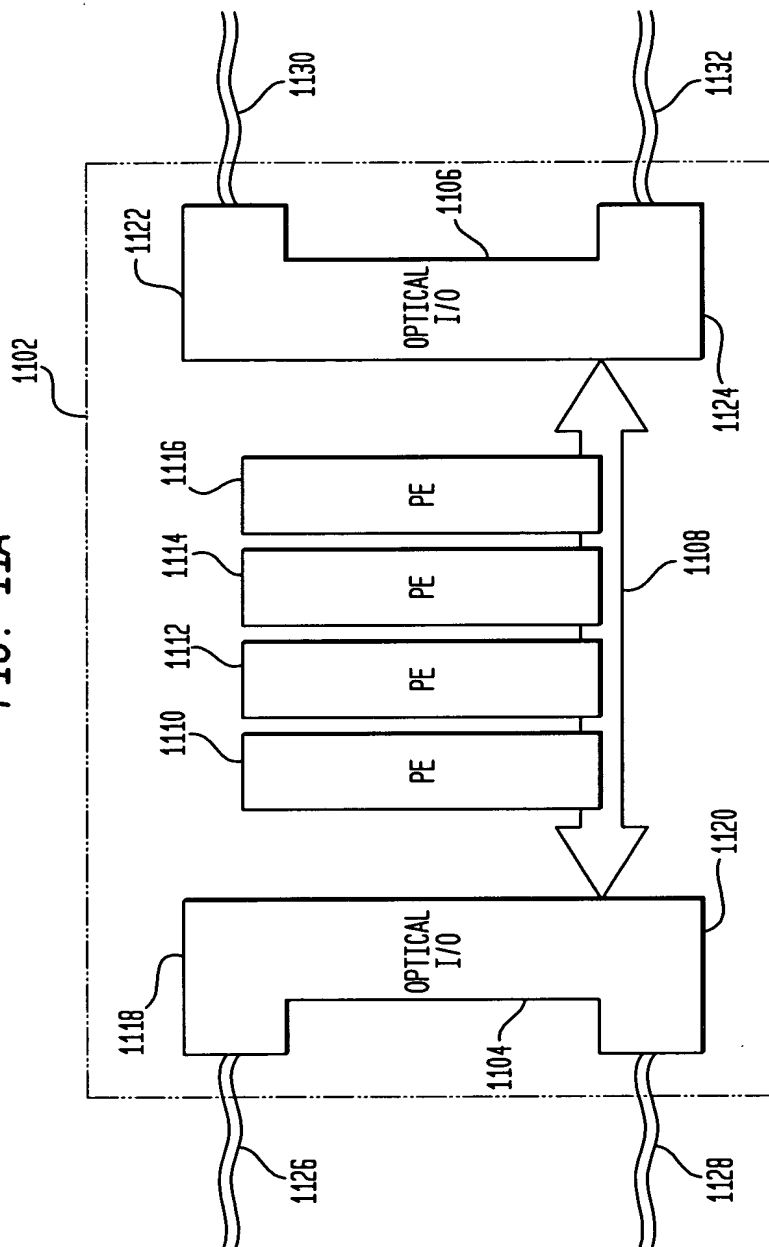


FIG. 11B

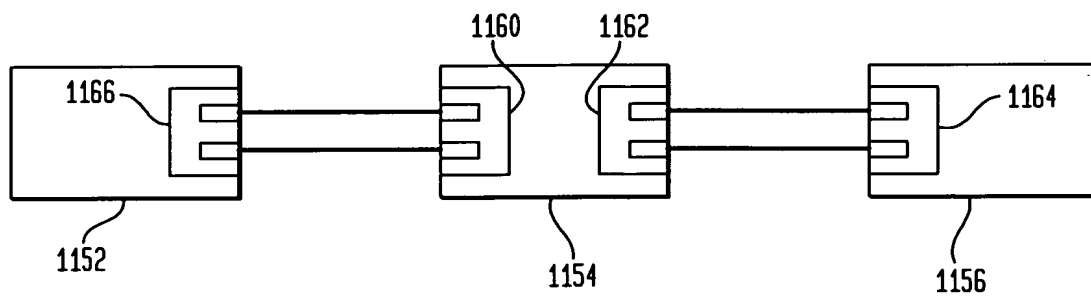


FIG. 11C

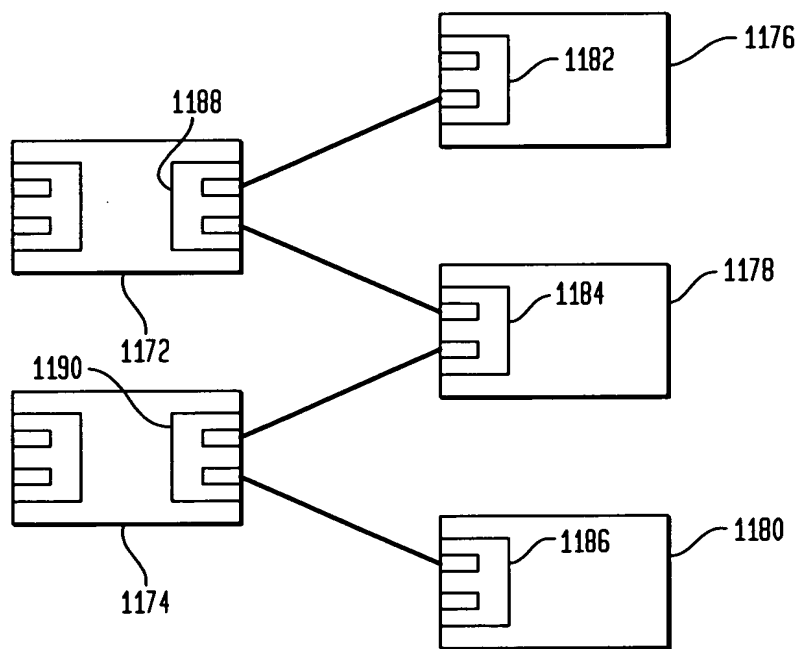


FIG. 12A

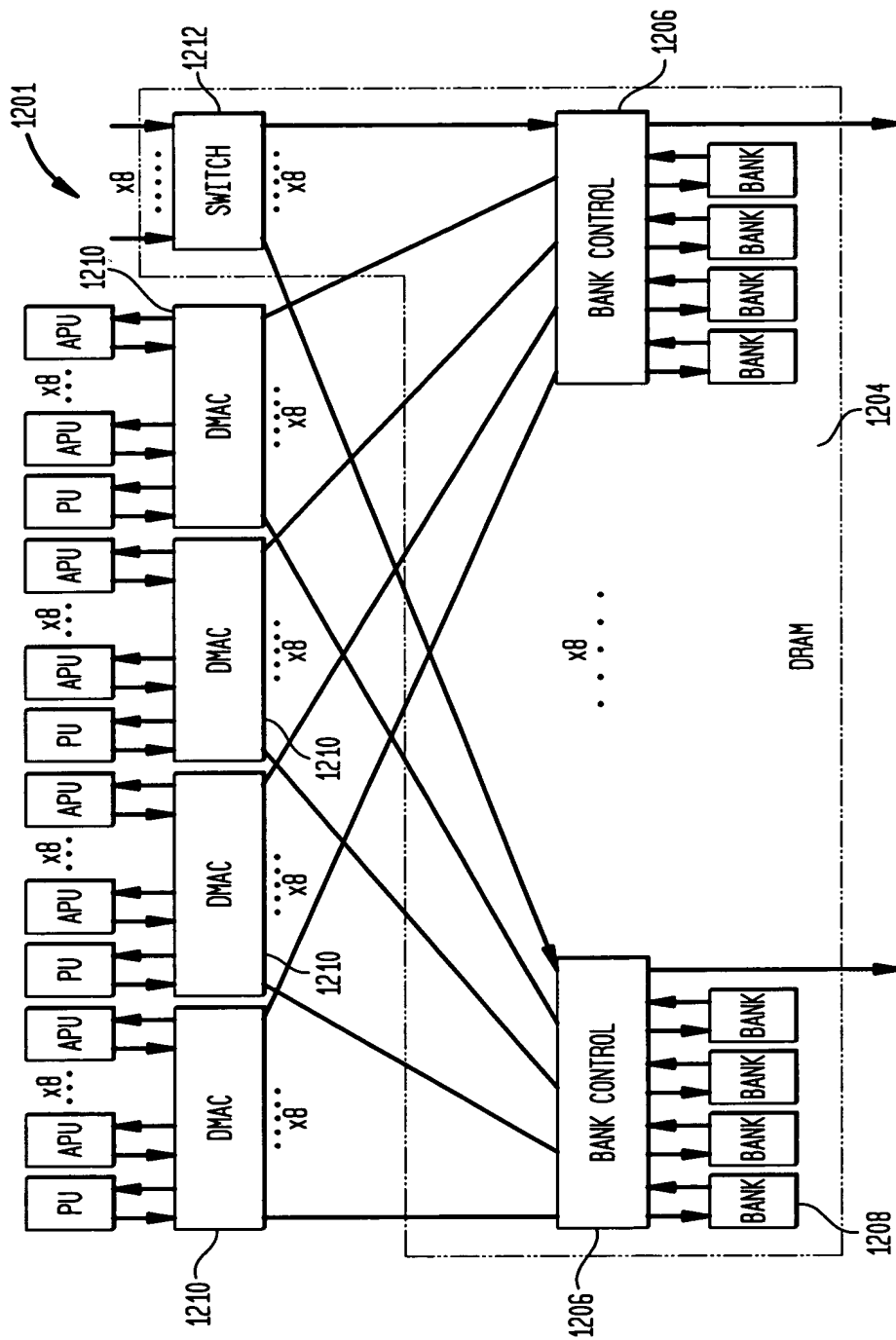


FIG. 12B

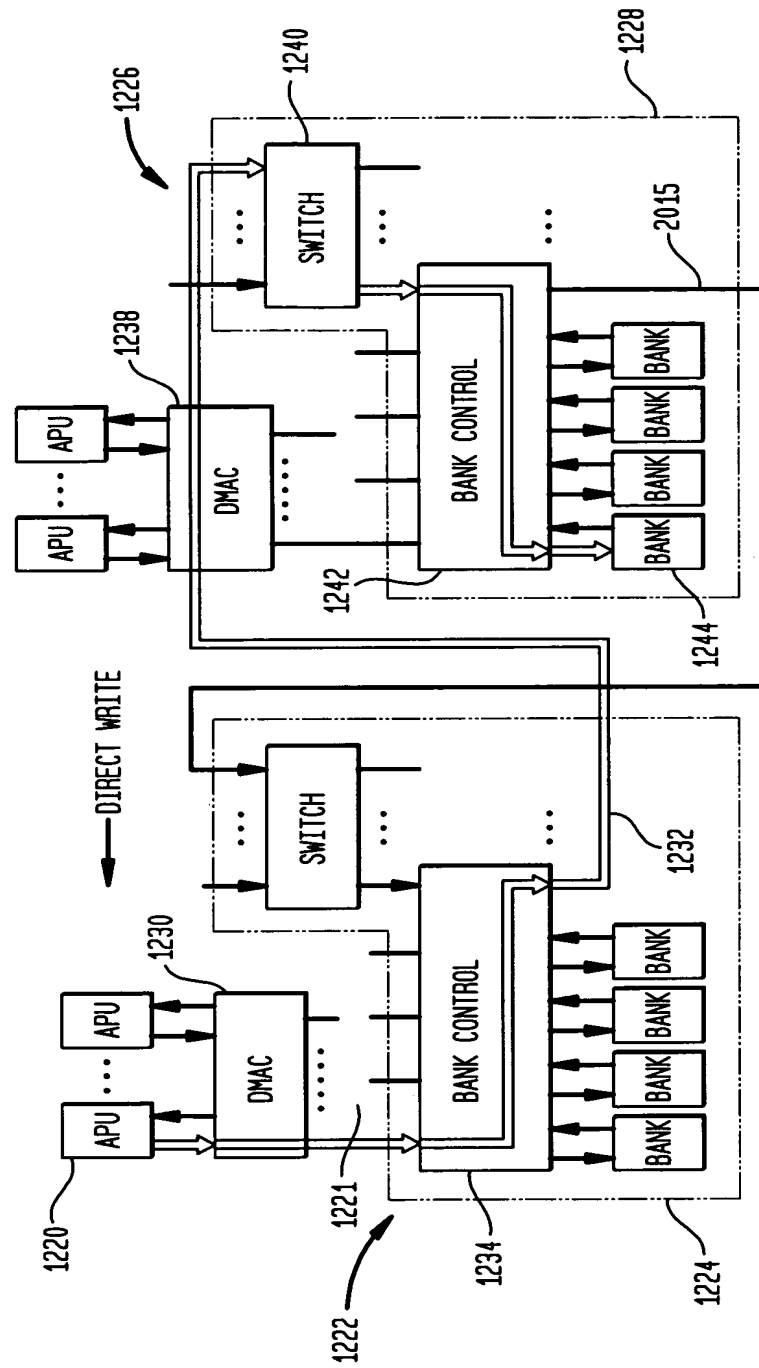
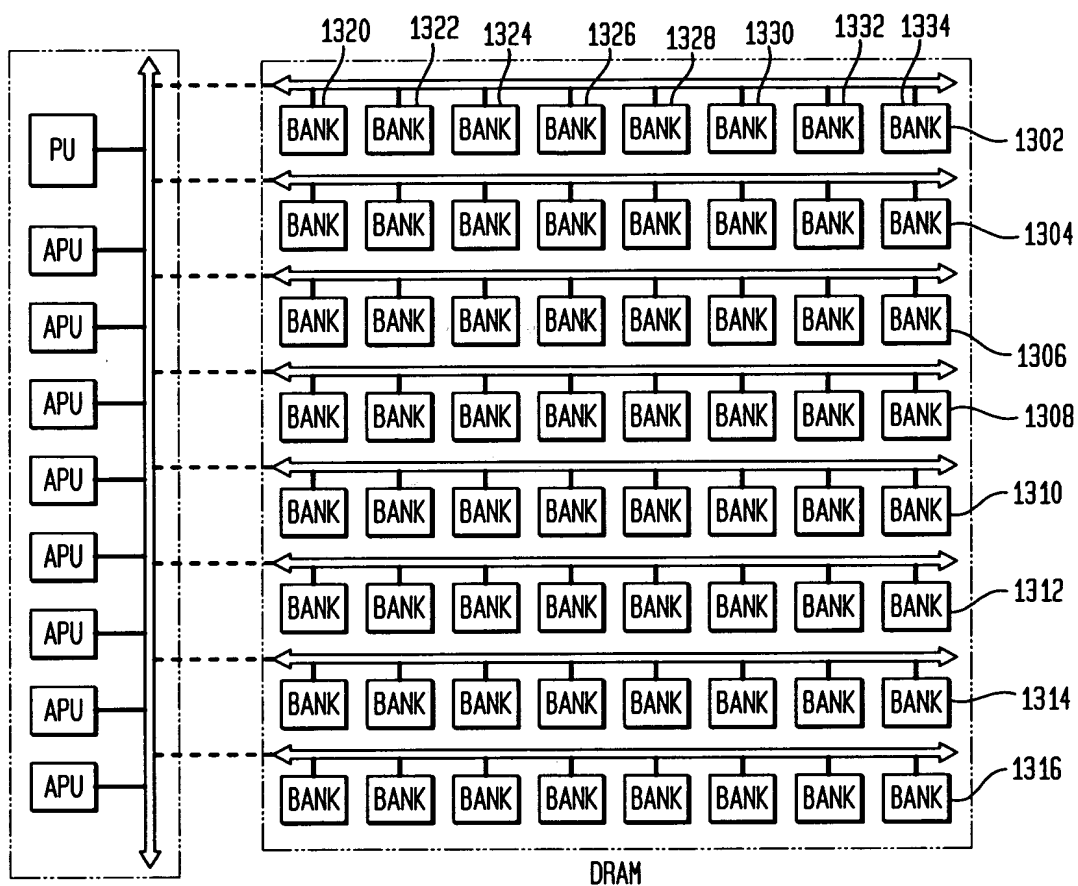


FIG. 13



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FIG. 14A

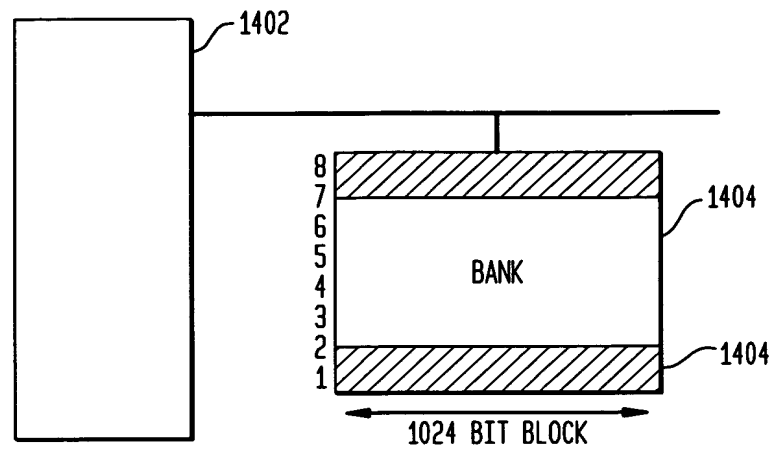


FIG. 14B

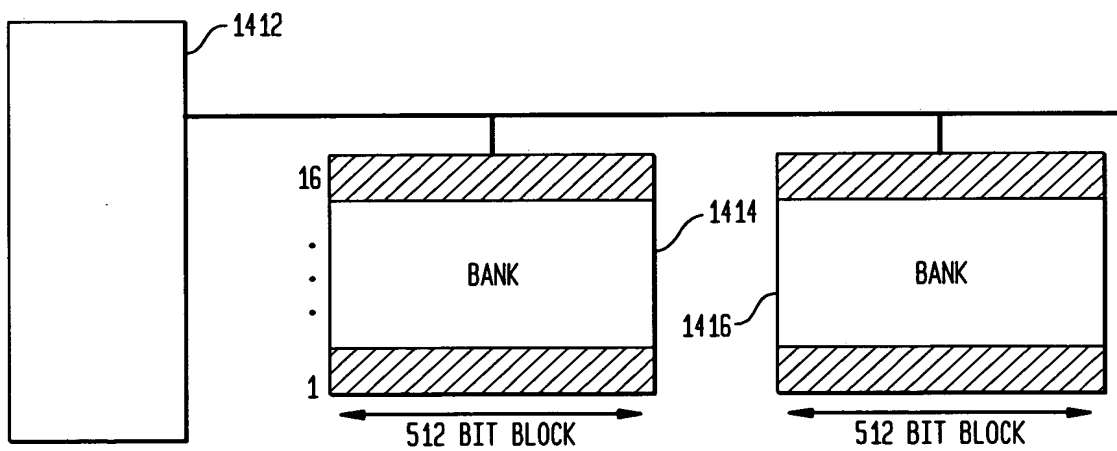




FIG. 15

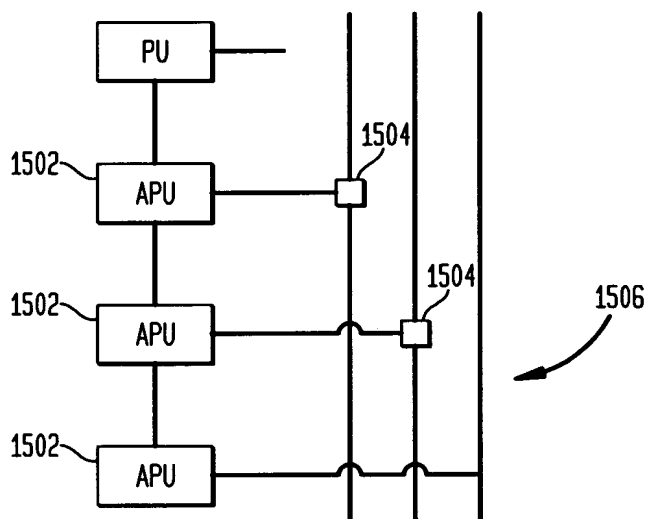


FIG. 16

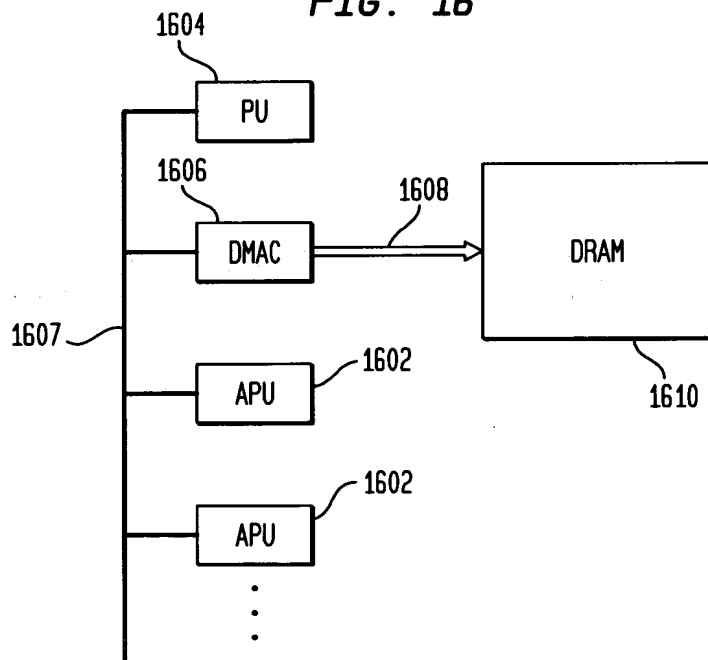


FIG. 17A

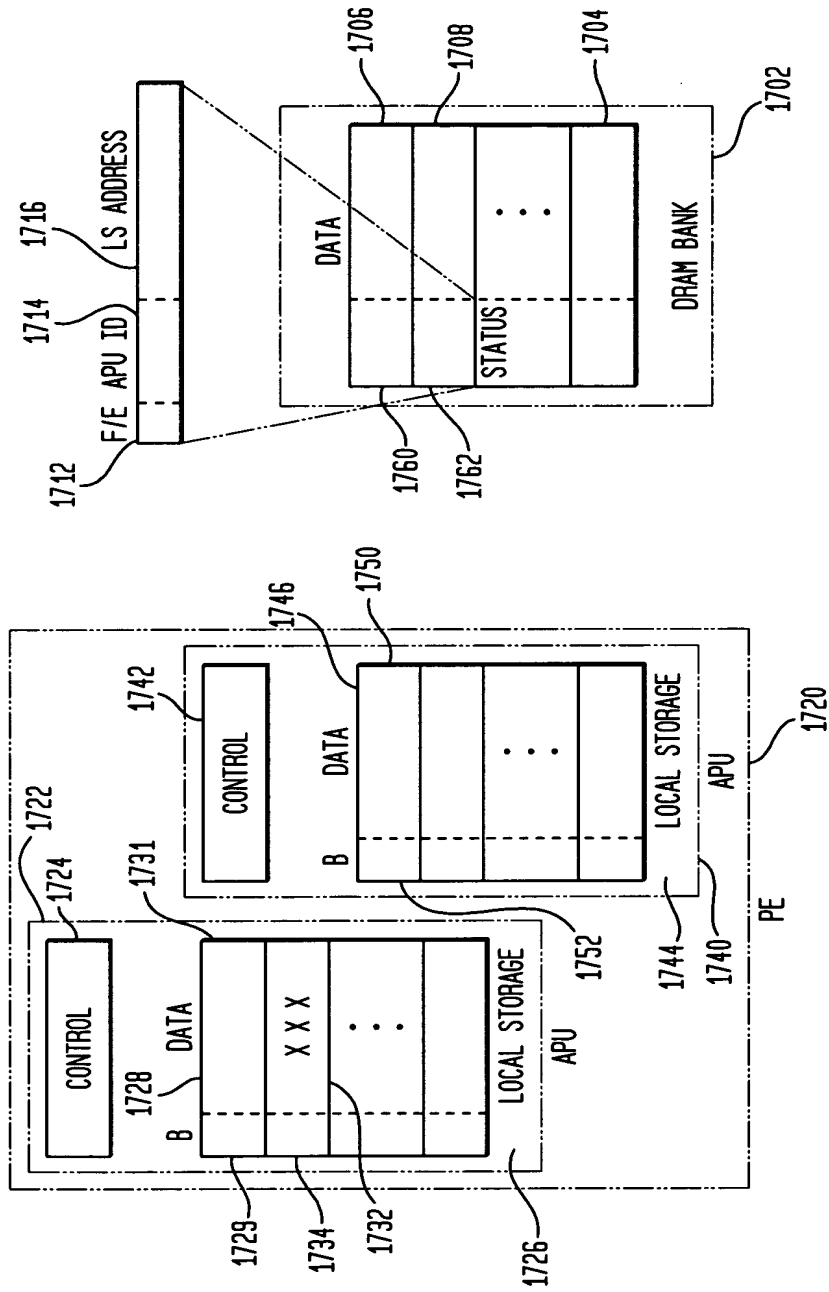


FIG. 17B

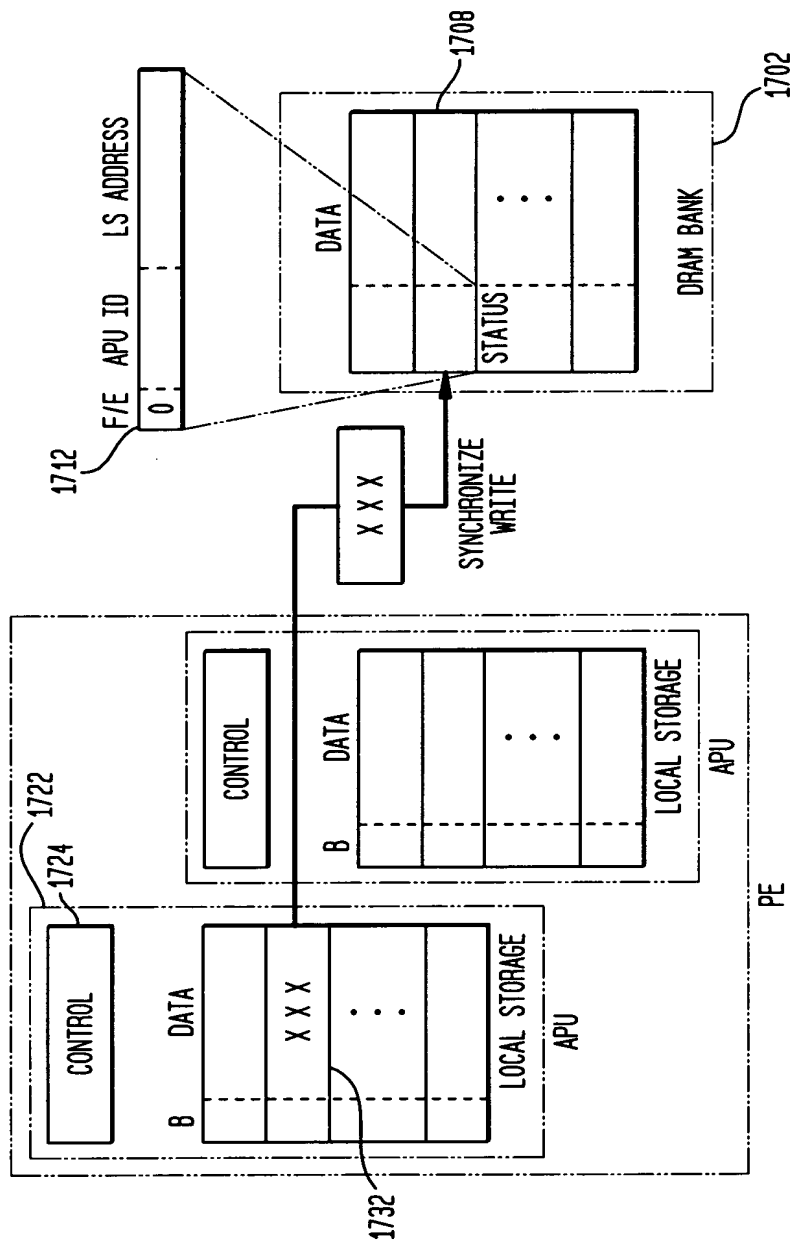


FIG. 17C

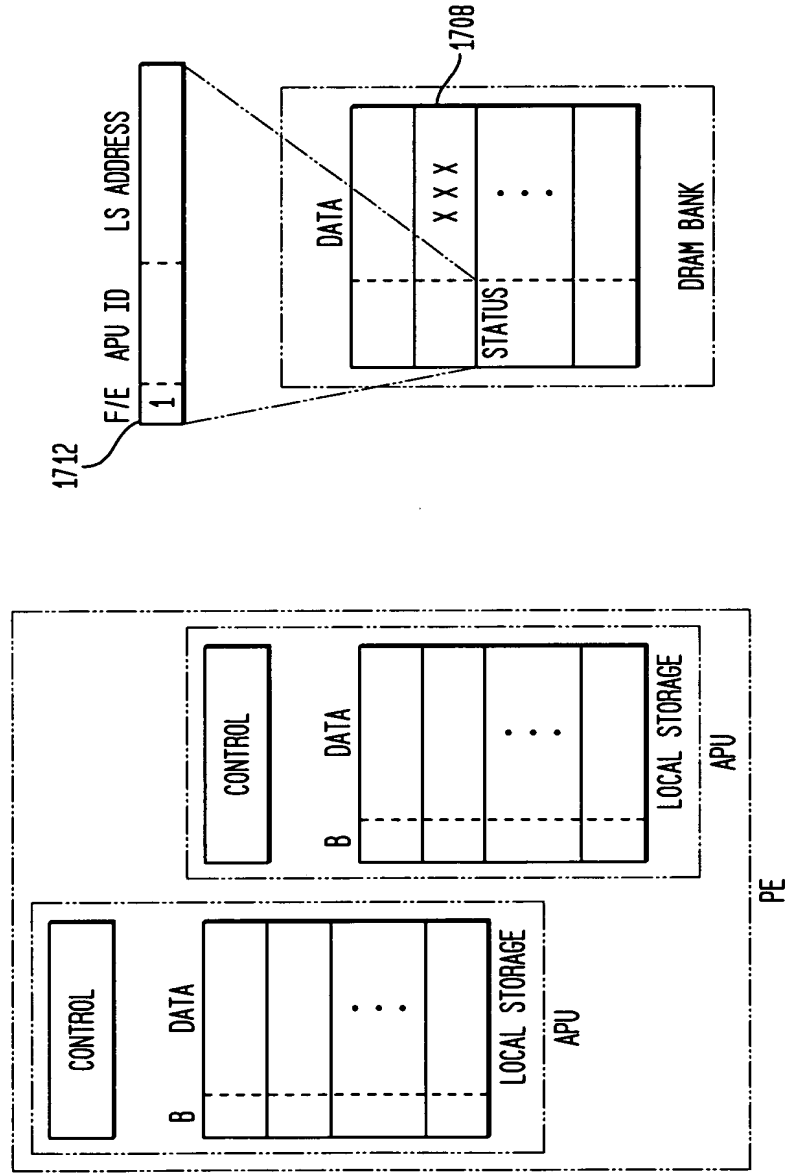




FIG. 17E

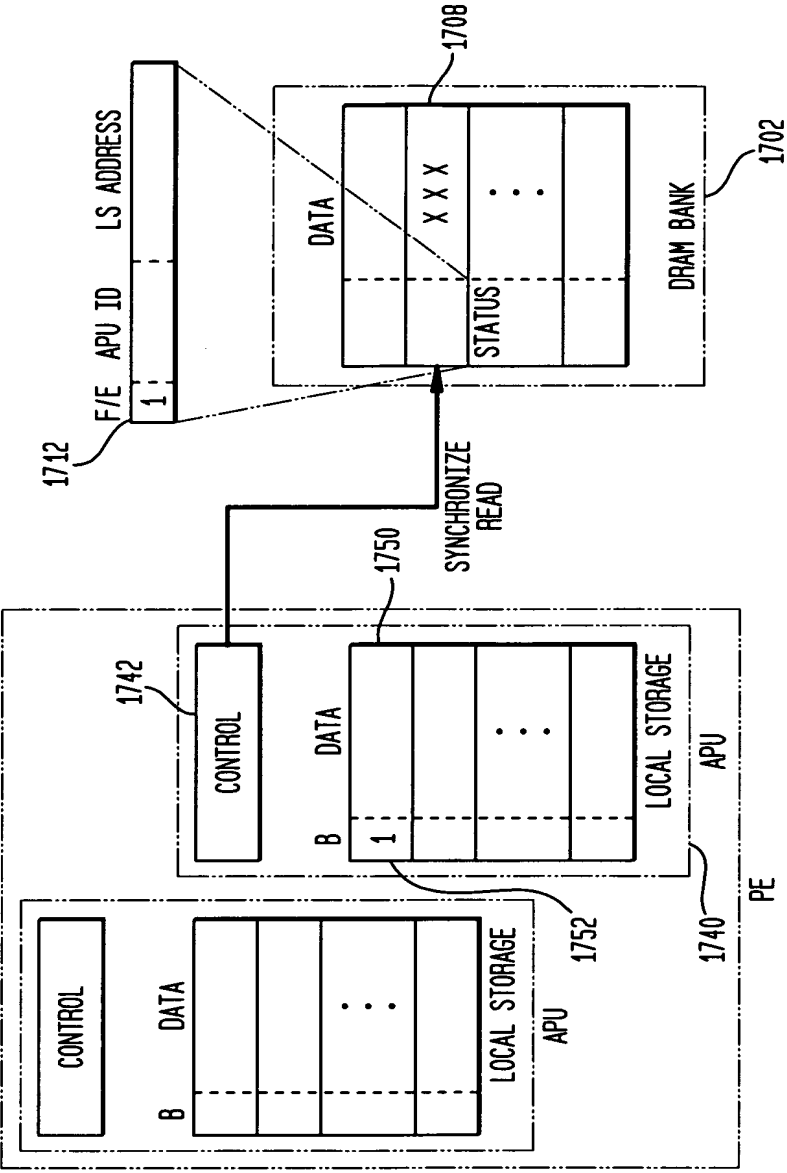


FIG. 17F

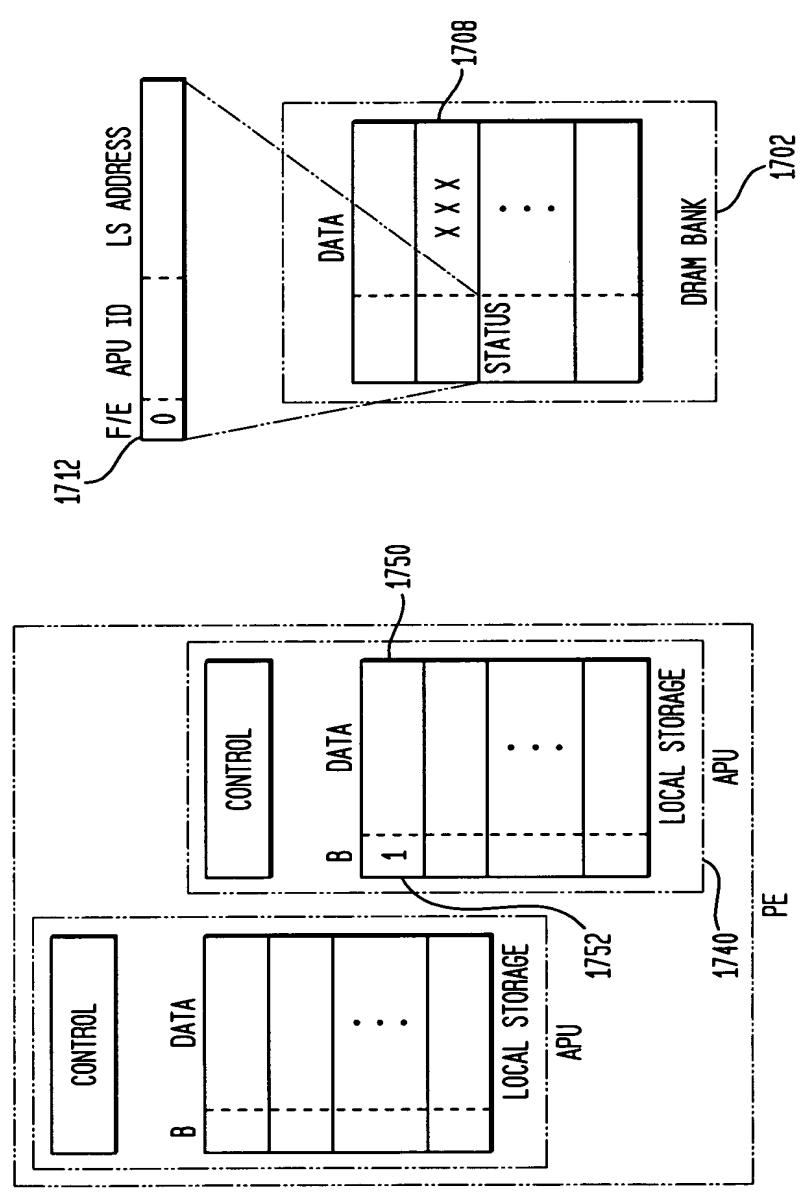


FIG. 17G

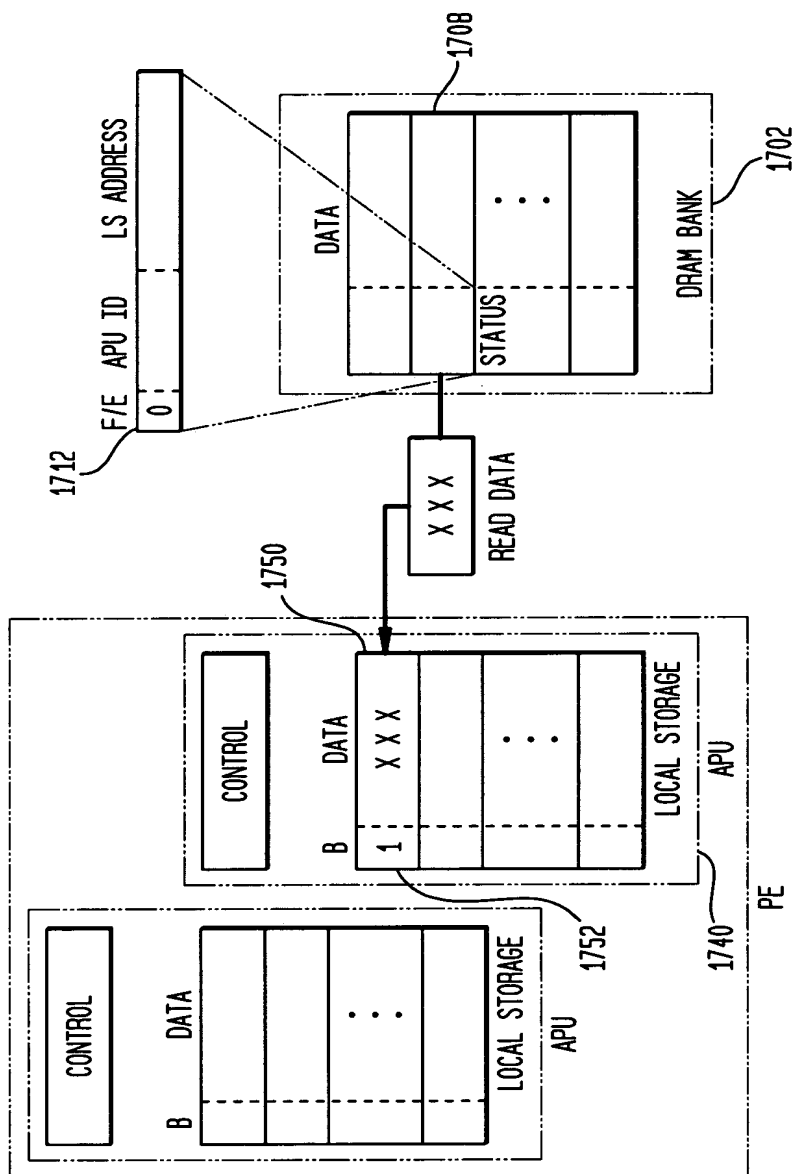




FIG. 17H

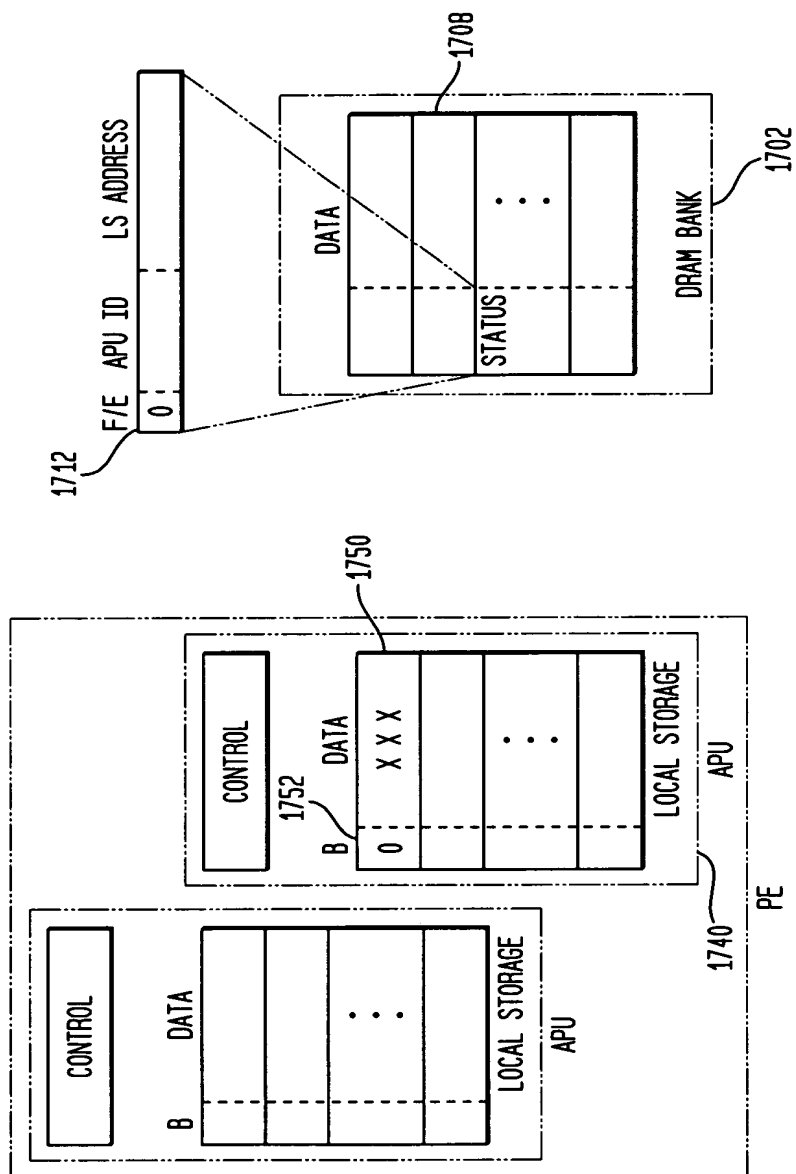


FIG. 17I

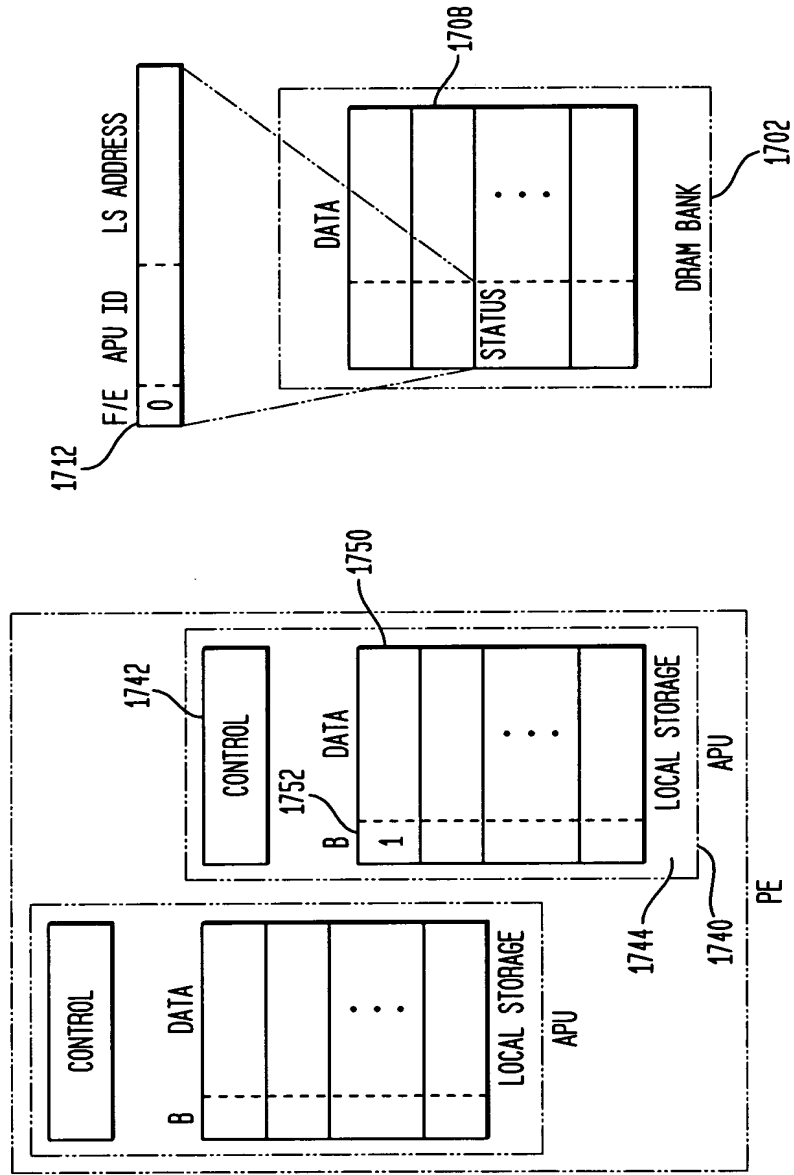


FIG. 17J

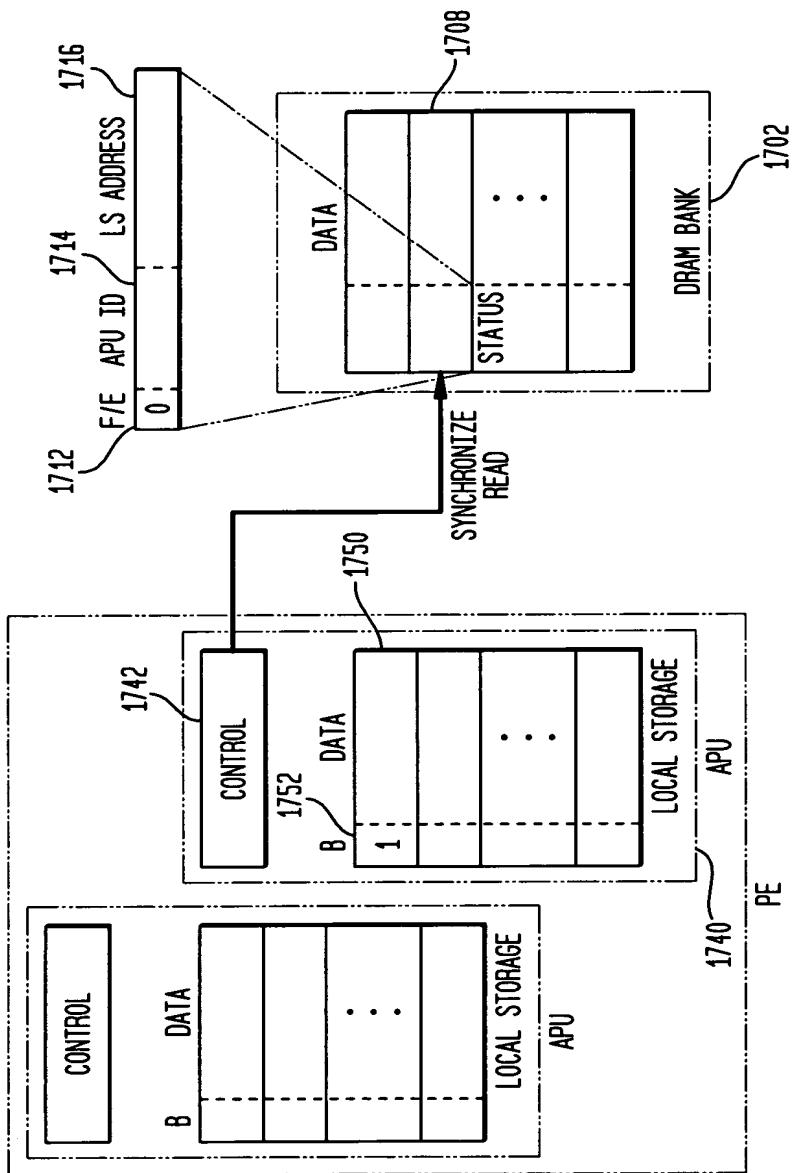


FIG. 17K

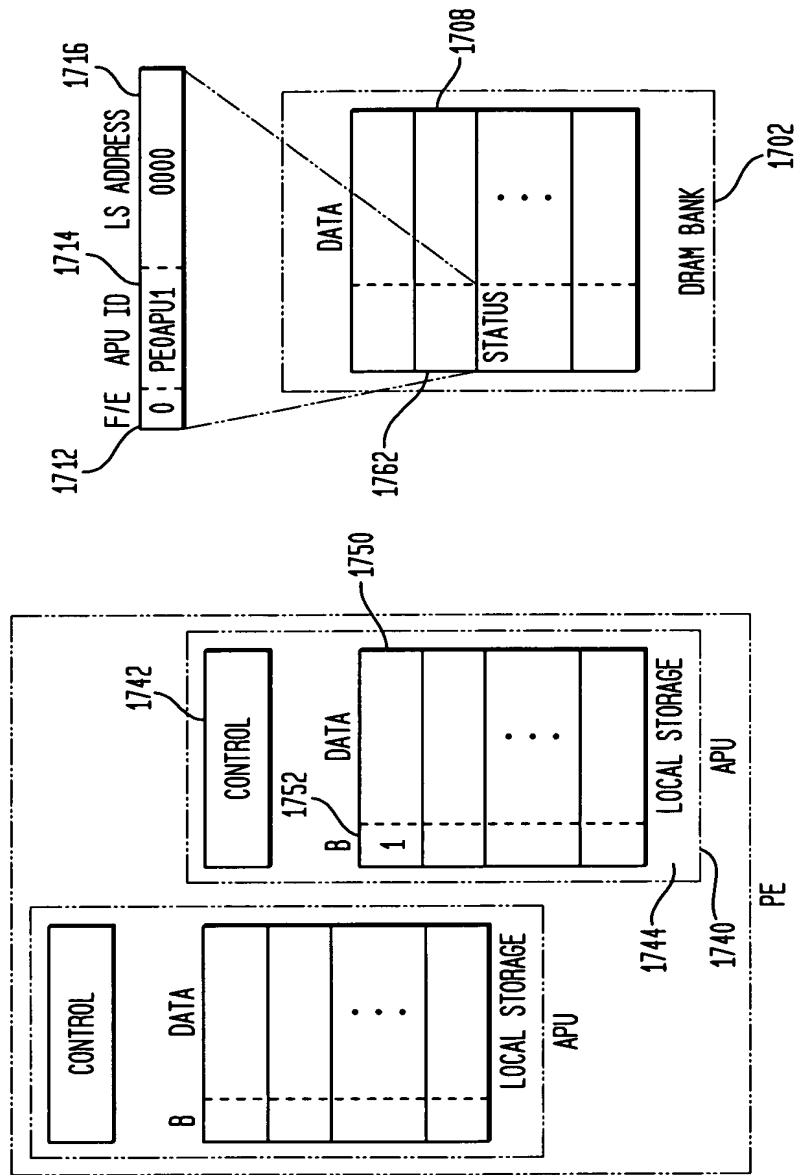


FIG. 17L

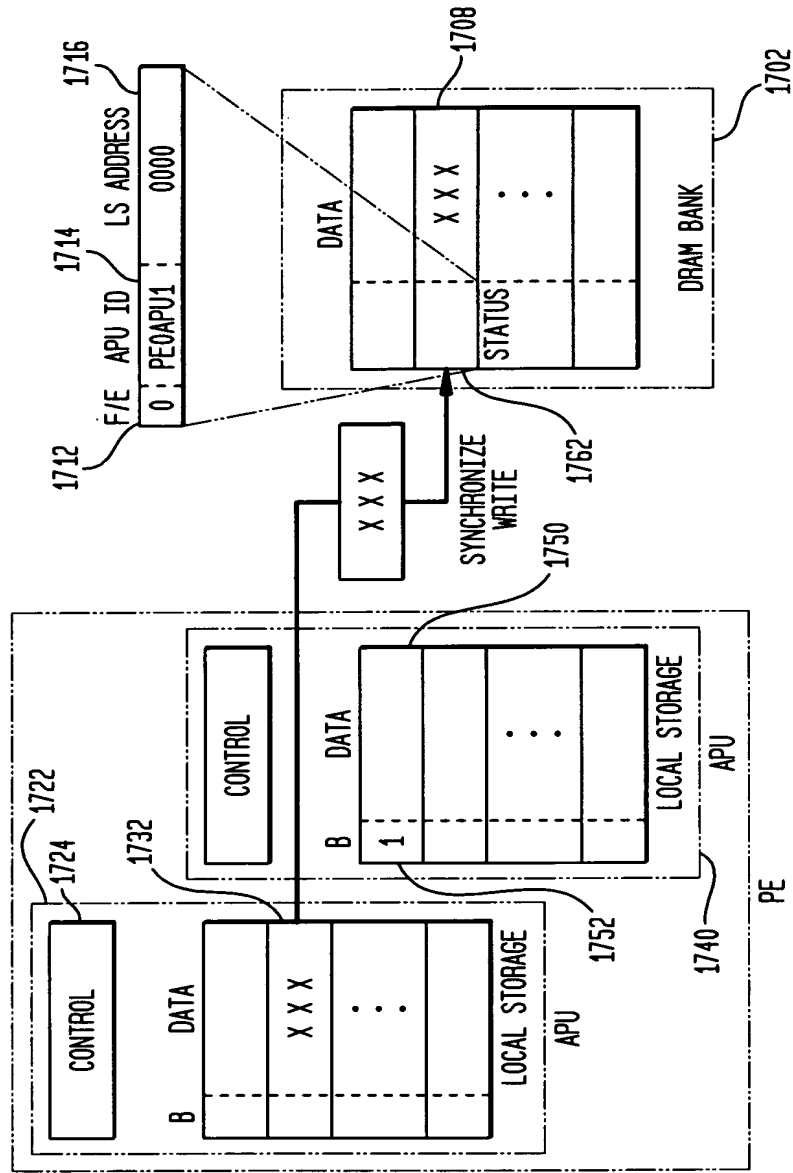




FIG. 17N

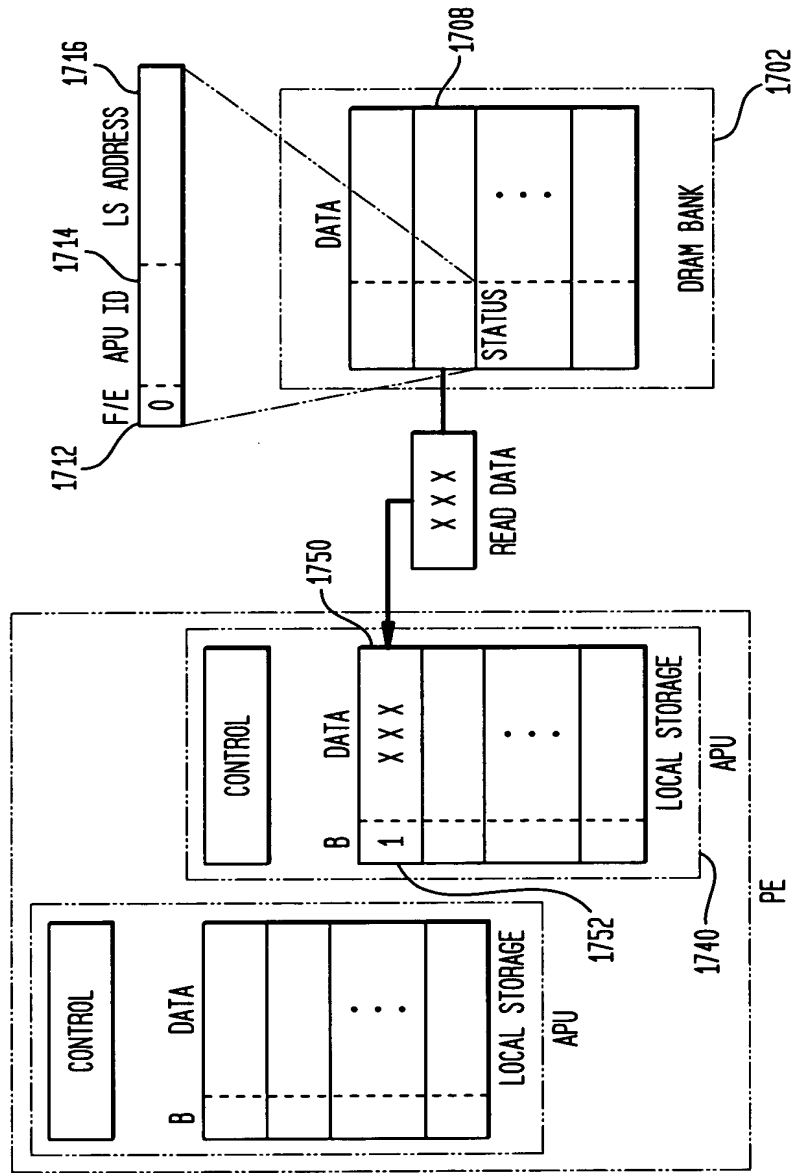
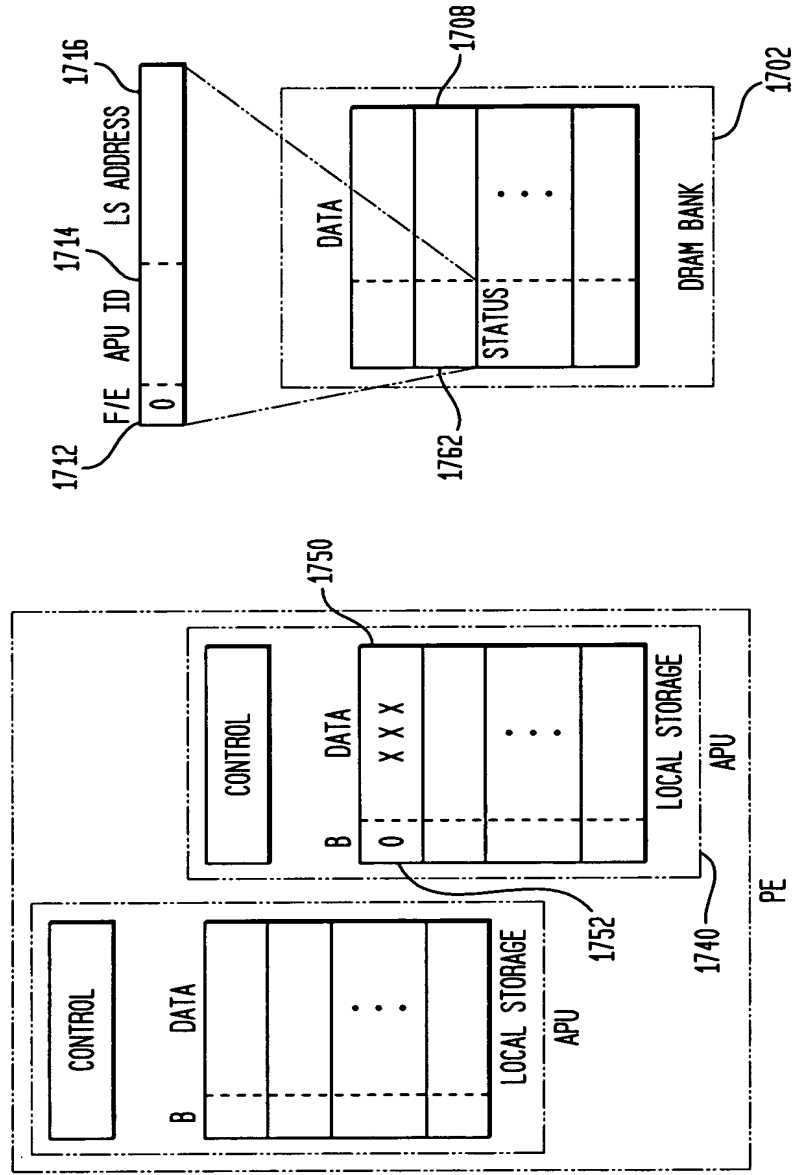
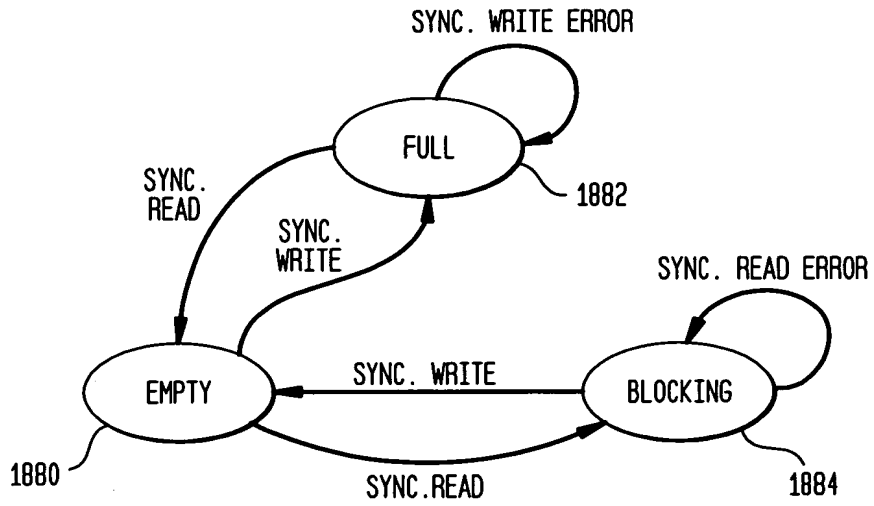


FIG. 170





**FIG. 18**



**FIG. 19**

1904 ID	1906	1908
0	APU KEY	KEY MASK
1	APU KEY	KEY MASK
2	APU KEY	KEY MASK
	⋮ ⋮ ⋮	
7	APU KEY	KEY MASK

1902

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FIG. 20

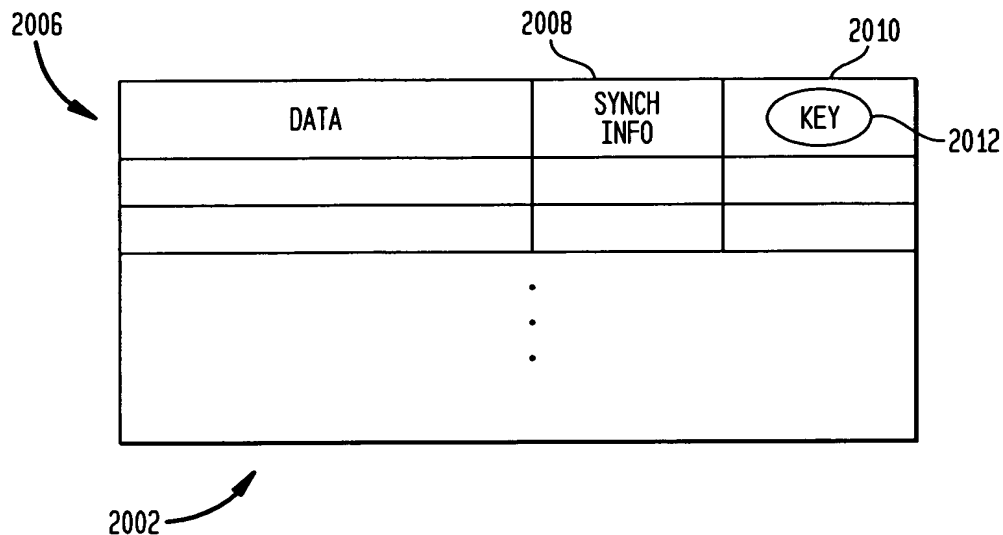


FIG. 21

ID	2106	2108	2110	2112
0	BASE	SIZE	ACCESS KEY	ACCESS KEY MASK
1	BASE	SIZE	ACCESS KEY	ACCESS KEY MASK
2	BASE	SIZE	ACCESS KEY	ACCESS KEY MASK
	⋮			
63	BASE	SIZE	ACCESS KEY	ACCESS KEY MASK

2104

2102

FIG. 22

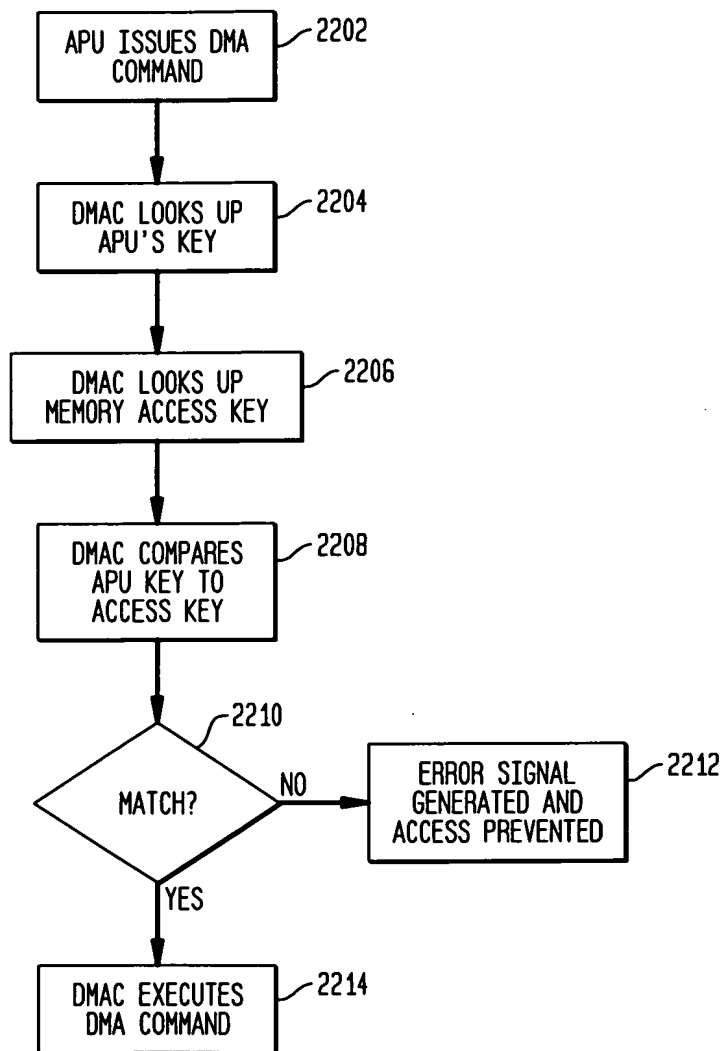
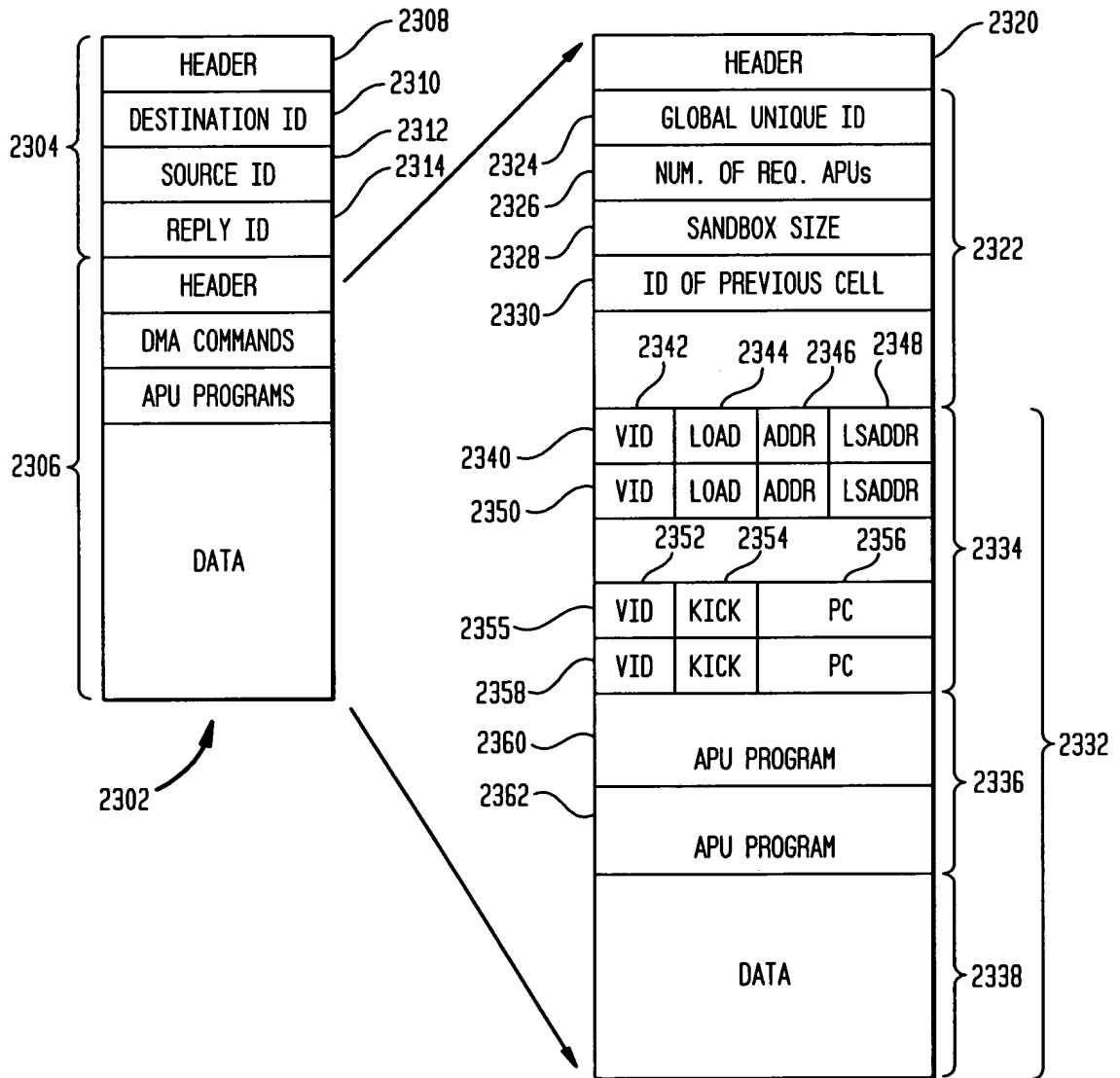


FIG. 23



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FIG. 24

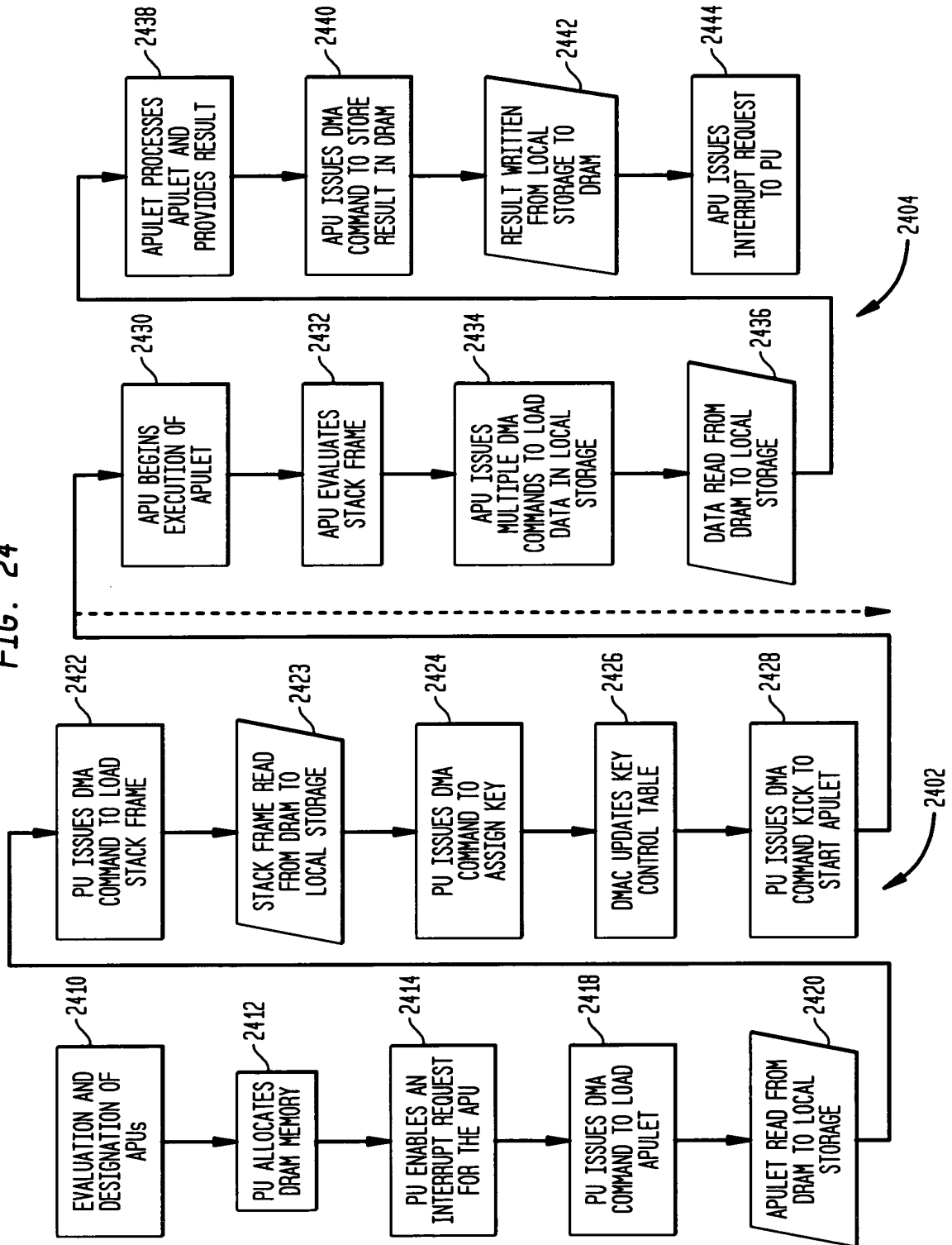


FIG. 25

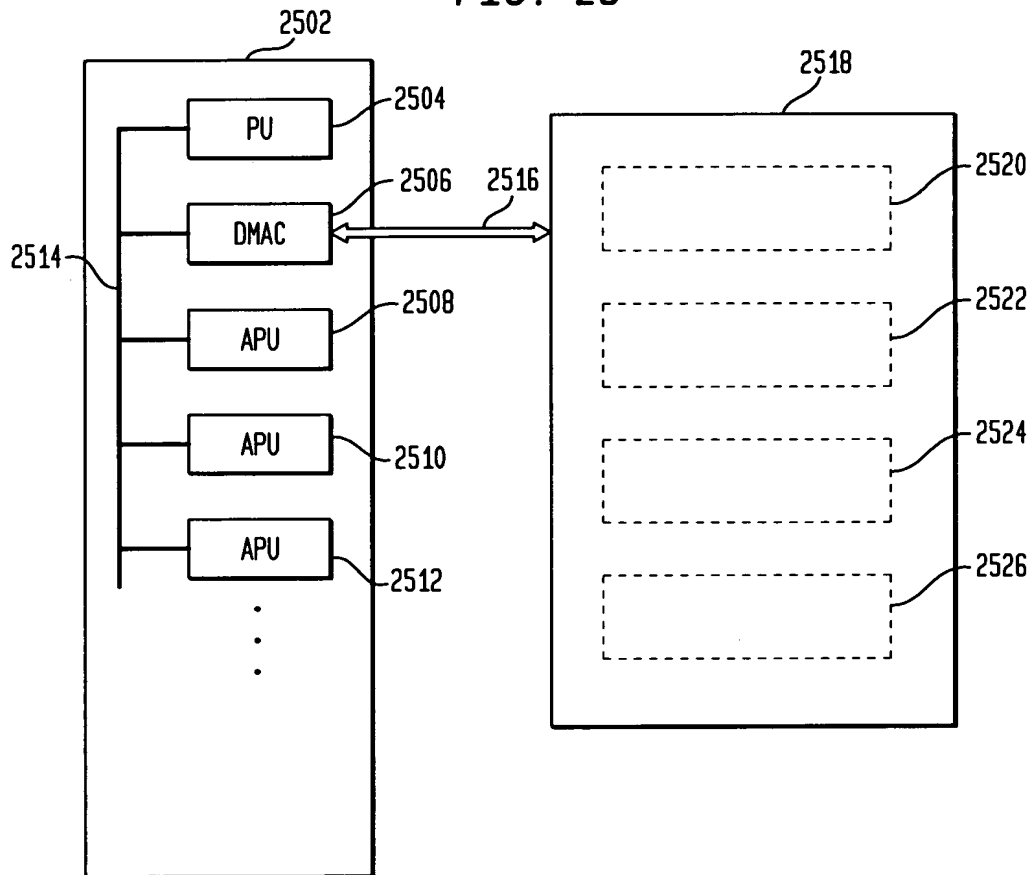
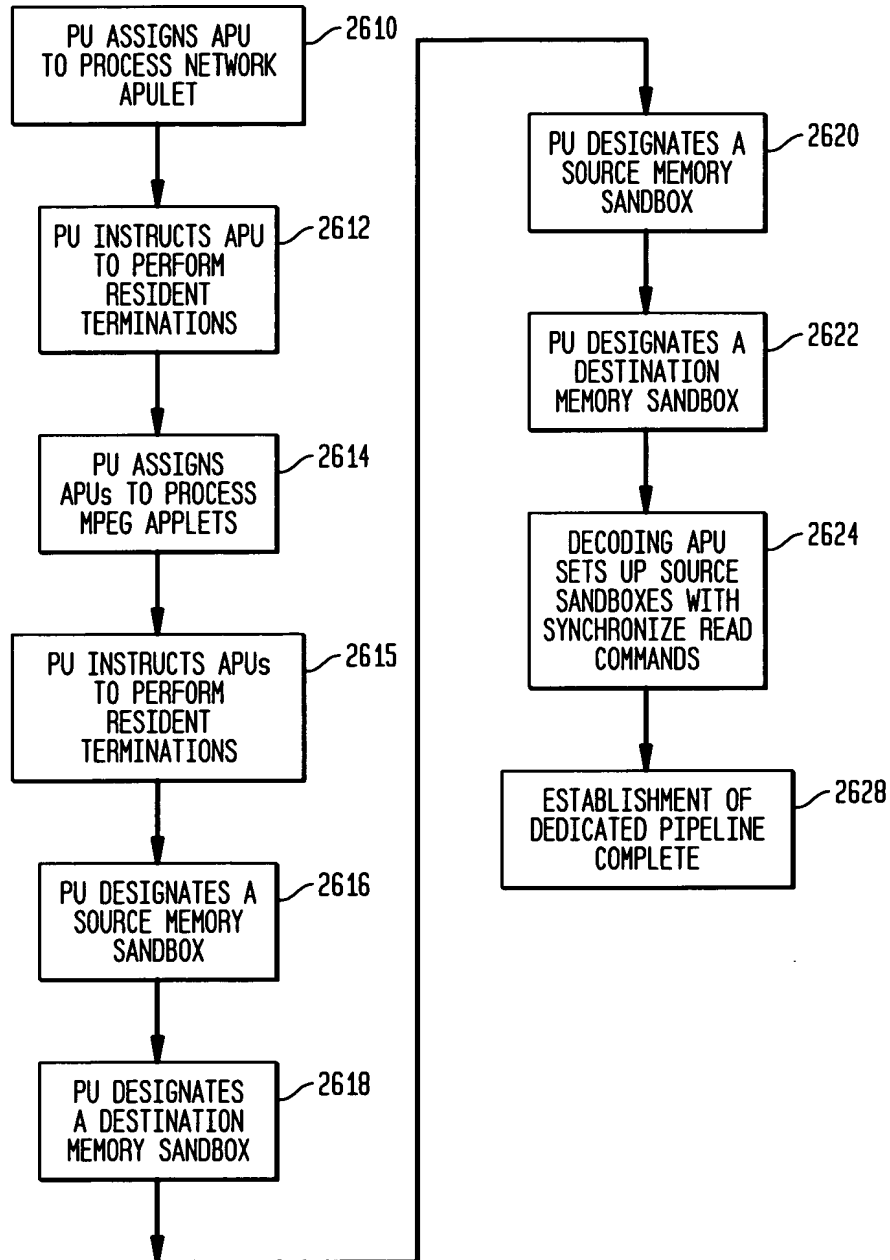


FIG. 26A



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FIG. 26B

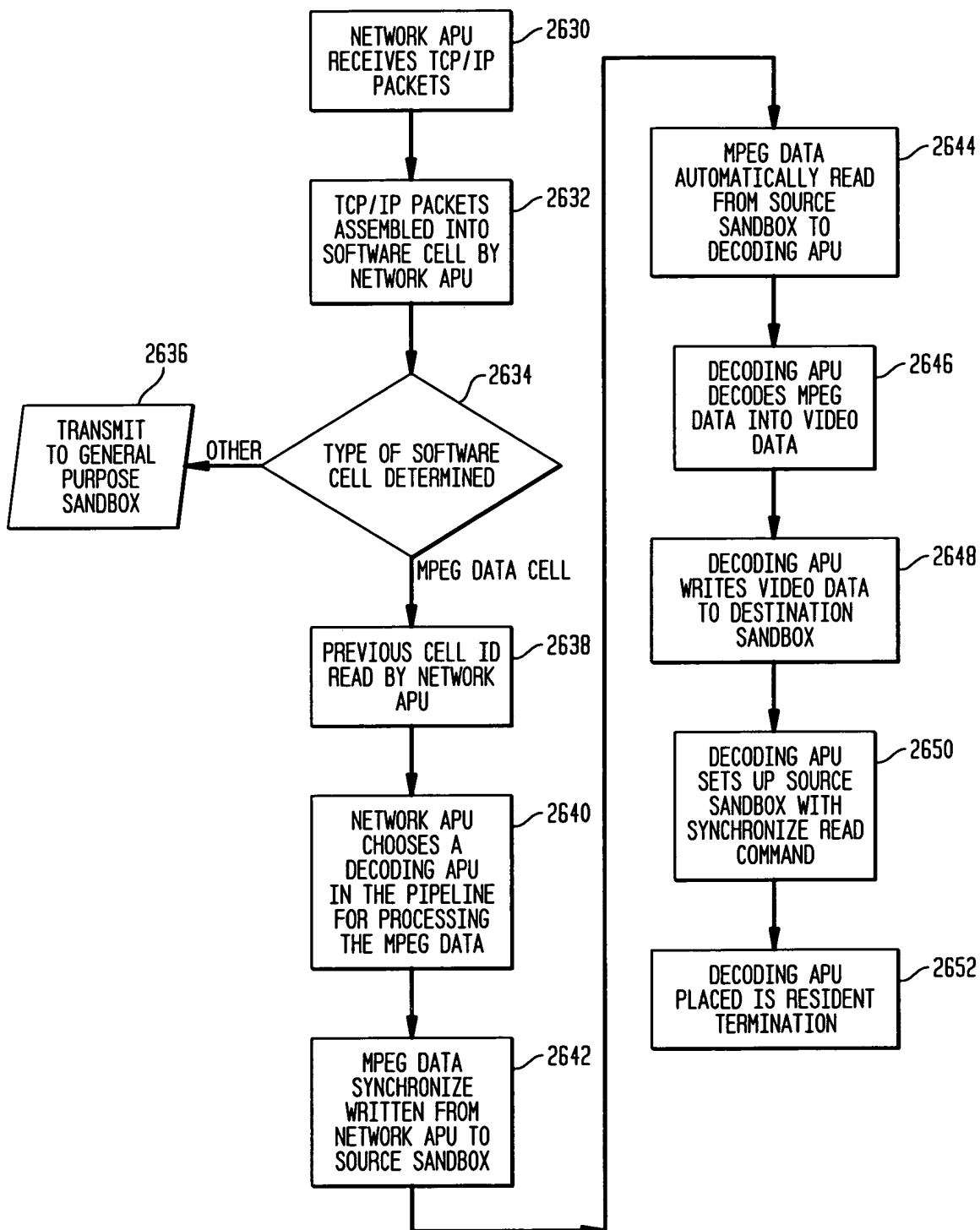




FIG. 27

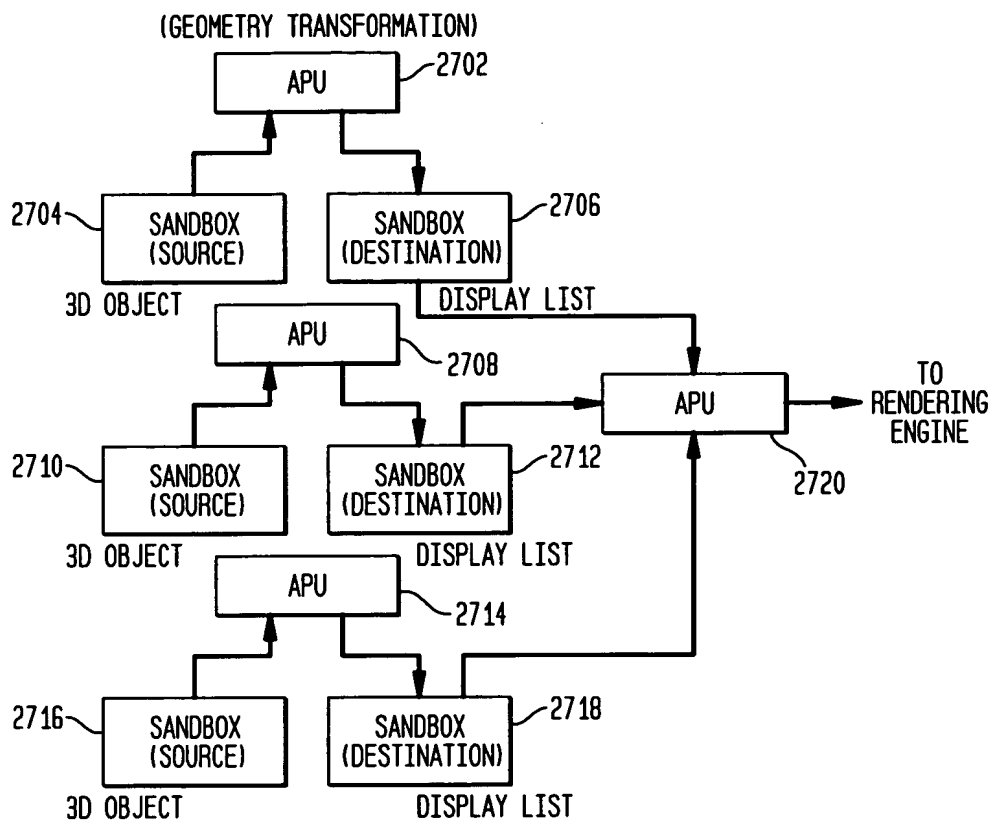


FIG. 28

